

Massachusetts Institute of Technology
Department of Electrical Engineering and Computer Science

6.002 – Electronic Circuits
Fall 2002

Outline

Date	Topic	Tut/Lab/Quiz	Reading:
R 9/4	Terminology, Resistors & Sources		1
L 9/5	Introduction & KVL & KCL		1, 2.1–2.3
R 9/6	Parallel & Series Resistors & Dividers		2.3–2.5, 2.8
9/9		Tutorial	
L 9/10	Node Analysis	Tutorial	3.1–3.3
R 9/11	Node Analysis Examples		3.1–3.3
L 9/12	Linearity & Superposition		3.5–3.7
R 9/13	Thevenin & Norton Equivalence		3.5–3.7
9/16		Tutorial	
L 9/17	Nonlinear Resistors & Small Signals	Tutorial	4
R 9/18	Nonlinear Resistors & Small Signals		4
L 9/19	Digital Abstraction & Boolean Logic		5
R 9/20	Digital Abstraction & Boolean Logic		5
9/23	Holiday		
L 9/24	MOS Switches & NMOS Digital Logic	Tutorial	6
R 9/25	MOS Switches & NMOS Digital Logic	Tutorial	6
L 9/26	Dependent Sources & Amplifiers	Quiz #1	2.6
9/27	No Class - Evening Quiz 9/26		
9/30		Tutorial	
L 10/1	MOS Amplifiers	Tutorial	7
R 10/2	MOS Amplifiers		7
L 10/3	Biasing & Small-Signal Analysis		7.7, 8
R 10/4	Biasing & Small-Signal Analysis		7.7, 8
10/7		Lab #1	
L 10/8	Small-Signal Circuit Models	Lab #1	8
R 10/9	Small-Signal Circuit Models	Lab #1	8
L 10/10	RC Networks & Step Responses	Lab #1	9, 10
R 10/11	RL Networks & Step Responses	Lab #1	9,10
10/14	Holiday		
10/15	Holiday		
R 10/16	Superposition & Int & Diff	Tutorial	

L 10/17	Pulse & Impulse Responses		10.6
R 10/18	Impulse Response Examples		10.6
10/21		Tutorial	
L 10/22	Energy & Power In NMOS Logic & CMOS	Tutorial	12
R 10/23	CMOS Logic Examples		12
L 10/24	PWM D/A Converter	Quiz #2	
10/25	No Class - Evening Quiz 10/24		
10/28		Lab #2	
L 10/29	LC Networks & Homogeneous Responses	Lab #2	13.1
R 10/30	RLC Networks & Homogeneous Responses	Lab #2	13.2–13.4
L 10/31	LC Networks & Step/Impulse Responses	Lab #2	13.5–13.12
R 11/1	RLC Networks & Step/Impulse Responses	Lab #2	13.5–13.12
11/4		Tutorial	
L 11/5	SSS & Frequency Response	Tutorial	14, B, C
R 11/6	SSS & Frequency Response		14, B, C
L 11/7	Impedance & Admittance		14, B, C
R 11/8	Impedance & Admittance	Lab #3	14, B, C
11/11	Holiday		
L 11/12	RLC Filters & Q	Lab #3	15
R 11/13	Time \leftrightarrow Frequency Response	Lab #3	15
L 11/14	Op-Amps & Amplifiers	Lab #3	16.1–16.4
R 11/15	Op-Amp Amplifiers	Lab #3	16.1–16.4
11/18		Tutorial	
L 11/19	Add & Sub & Int & Diff	Tutorial	16.5–16.7, 16.10
R 11/20	Op-Amp Filters		16.5–16.7, 16.10
L 11/21	Positive Feedback & Oscillators	Quiz #3	16.8
11/22	No Class - Evening Quiz 11/21		
11/25		Tutorial	
L 11/26	MOS Differential Amplifiers	Tutorial	8.2
R 11/27	Special Op-Amp Circuits		
11/28	Holiday		
11/29	Holiday		
12/2		Lab #4	
L 12/3	Diodes	Lab #4	17
R 12/4	Diodes & RC Networks	Lab #4	17.4
L 12/5	Diodes & LC Networks	Lab #4	
R 12/6	Diodes & Op-Amps	Lab #4	
12/9		Tutorial	
L 12/10	AM Radio Example	Tutorial	
R 12/11	Power Electronics		