# Massachusetts Institute of Technology <br> Department of Electrical Engineering and Computer Science 

# 6.002 - Circuits and Electronics <br> Spring 2003 

## Handout S03-024 - Homework \#5

Issued: Wed. Mar 5
Due: Fri. Mar 14

Problem 5.1: A semiconductor diode can be used as a variable attenuator:


In this diagram the current source $I$ establishes the steady DC current in the diode (the operating-point current), while $v_{i}$ is the input to be attenuated. $R$ is sufficiently large to insure that the operating-point current in the diode is approximately $I$.

The $i-v$ characteristic of the diode is $i_{D}=I_{S}\left(e^{\frac{q v_{D}}{k T}}-1\right)$.
(A) Derive an expression for the incremental conductance $g$ of the diode. Assume that $e^{\frac{q v_{D}}{k T}} \gg 1$. That is, the current $I$ is large enough so that the diode is well into the forward, or conducting, region.
(B) Show that $g$ can be expressed as $g=I\left(\frac{q}{k T}\right)$.
(C) Draw a complete incremental (small-signal) model for the circuit above. Explain why the current source does not appear in this model.
(D) Derive an expression for the attenuation of the circuit. that is, derive an expression for $A$ :

$$
A=\frac{v_{d}}{v_{i}}
$$

where $v_{d}$ is the incremental component of the diode voltage. Assume $R \gg \frac{1}{g}$ and show that the attenuation is proportional to $I$.
(E) If the magnitude of $v_{d}$ is too large, this voltage will be distorted because the linear approximation implied in the definition of $g$ becomes invalid. What is a reasonable upper bound for $\left|v_{d}\right|$ ? Hint: Think Taylor series expansion of $i_{D}=f\left(v_{D}\right)$.

Problem 5.2: The Gizmo whose circuit symbol is shown below is an electronic valve. The output characteristic of the Gizmo is shown as well.



Values of $v_{G C}$ are in volts

The Gizmo functions as an amplifier in the circuit shown below:

(A) When no signal is present, i.e., when $v_{I N}=V_{I N}$, the circuit is to have a current at $P$ of $i_{P}=3 \mathrm{~mA}$. That is, the quiescent or operating-point current is to be $I_{P}=3 \mathrm{~mA}$.

Specify the operating-point value of $V_{I N}$ that will produce the desired operating point.
Hint: Think load line.
(B) What is the value of $V_{P C}$ at the operating point?
(C) An incremental model for the Gizmo at any operating point in the output characteristics is shown below.

$r_{p}$ and $\mu$ are constants which can be derived
directly from the output characteristics above.
(D) Assume $r_{p}=1.5 \mathrm{k} \Omega$ and $\mu=15$.
(These are not the correct values corresponding to the output characteristics above, but will do for this part of the problem)

Draw a complete incremental model for the amplifier circuit shown above. Label all component values.
(E) Calculate the incremental voltage gain of this amplifier:

$$
A_{v}=\frac{v_{o u t}}{v_{i n}}
$$

Where $v_{\text {in }}$ and $v_{\text {out }}$ are the incremental components of the input and output voltages respectively.
(F) For extra credit, determine the approximate correct values of $r_{p}$ and $\mu$ from the output characteristics.

Problem 5.3: A FET is used as the control valve in the amplifier circuit shown below.


Note: $\quad v_{i} \neq v_{g s}$ for this circuit!
(A) Draw and label the complete incremental model of this amplifier.
(B) Derive an expression for the incremental voltage gain $A_{v}=\frac{v_{o}}{v_{i}}$ in terms of $R_{S}, R_{L}$, and the transconductance $g_{m}$.
(C) Assume now that the source of the supply voltage $V$ has been damaged and produces in addition to $V$ a noise source $v_{n}$. The defective supply can be modeled as:


Draw and label an incremental model for the amplifier which takes into account this noisy voltage source.
(D) Using that model, determine the component of $v_{o}$ that results from $v_{n}$.

Hint: Think superposition.

