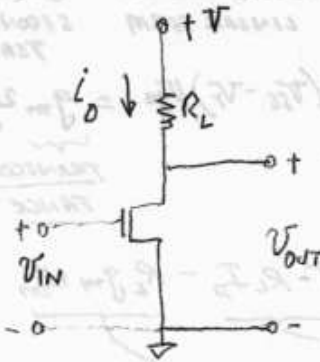


NOTES FOR 6.002 LECTURE #8 TUESDAY, MARCH 4, 2003

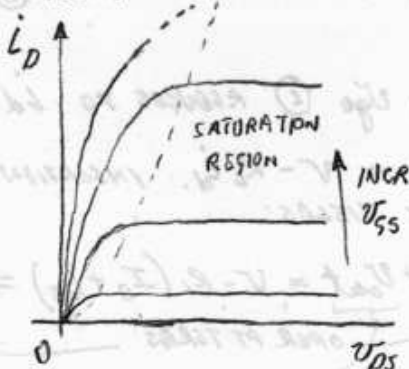
READ: 8.1 - 8.2.3

THERE ARE TWO VIDEOS ON THE 6.002 WEB SITE ON THE TOPICS OF LARGE- AND SMALL-SIGNAL ANALYSIS OF FET AMPLIFIERS

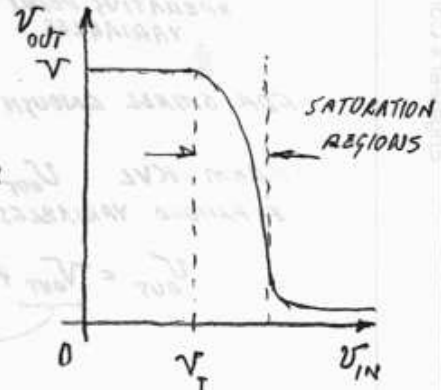
A FET AS AN ELECTRONIC VALVE IS AN AMPLIFIER OF SIGNALS
WHAT ABOUT ANALYSIS?



CIRCUIT
 $V_{IN} = V_{GS}, V_{OUT} = V_{DS}$



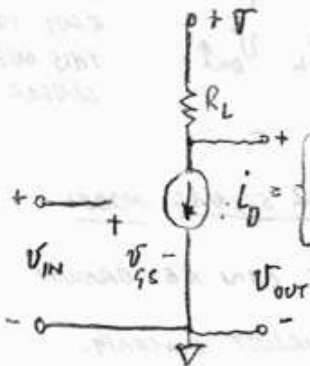
OUTPUT CHARACTERISTICS



TRANSFER CHARACTERISTIC

IN THE SATURATION REGION SMALL CHANGES IN V_{IN} PRODUCE LARGE CHANGES IN V_{OUT} VOILA! VOLTAGE GAIN

INSERT SQUARE-LAW FET MODEL INTO CIRCUIT:



I_D IS A DEPENDENT SOURCE

$$I_D = \begin{cases} 0 & \text{FOR } V_{GS} < V_T \\ \frac{K}{2} (V_{GS} - V_T)^2 & \text{FOR } (V_{GS} - V_T) > V_{DS} \end{cases}$$

← FOCUS ON THIS DOMAIN WHERE AMPLIFICATION OCCURS.

NOTATION: REPRESENT EVERY VARIABLE AS THE SUM OF TWO COMPONENTS: INCREMENTAL OR SMALL-SIGNAL VARIABLES MEASURED DEPARTURES FROM AN OPERATING POINT.

$$V_{GS} = \overbrace{V_{GS}} + \overbrace{v_{gs}}$$

$$V_{DS} = \overbrace{V_{DS}} + \overbrace{v_{ds}}$$

$$I_D = \overbrace{I_D} + \overbrace{i_d}$$

QUIESCENT OR OPERATING-POINT VALUES

INSERT THESE VARIABLES INTO SQUARE-LAW MODEL:

$$I_D = \frac{K}{2} (V_{GS} - V_T)^2 \rightarrow I_D + i_d = \frac{K}{2} (V_{GS} - V_T + v_{gs})^2$$

$$\textcircled{1} I_D + i_d = \frac{K}{2} (V_{GS} - V_T)^2 + K(V_{GS} - V_T)v_{gs} + \frac{K}{2} v_{gs}^2$$

THIS EQUATION MUST HOLD FOR ALL VALUES OF v_{gs} INCLUDING $v_{gs} = 0$, YIELDING

$$I_D = \frac{K}{2} (V_{GS} - V_T)^2 \quad \text{SUBTRACT FROM} \quad \textcircled{1} \rightarrow i_d = K(V_{GS} - V_T)v_{gs} + \frac{K}{2} v_{gs}^2$$

OPERATING POINT VARIABLES

LINEAR TERM

SECOND ORDER TERM

FOR SMALL ENOUGH v_{gs} $\textcircled{2}$ REDUCES TO $i_d \approx K(V_{GS} - V_T)v_{gs} = g_m v_{gs}$

FROM KVL $V_{out} = V - R_L i_d$. INSERTION OF EXPANDED VARIABLES YIELDS:

TRANSCONDUCTANCE

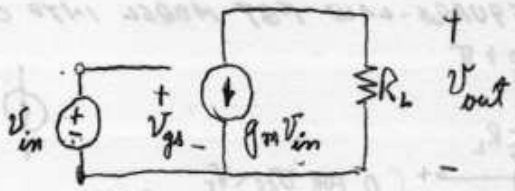
$$V_{out} = \underbrace{V_{out} + v_{out}}_{\text{OPER. PT TERMS}} = V - R_L (I_D + i_d) = \underbrace{V - R_L I_D}_{\text{INCREMENTAL TERMS}} - R_L g_m v_{in}$$

SUBTRACTING OUT THE OPERATING-POINT TERMS YIELDS:

$$v_{out} = -g_m R_L v_{in}$$

SMALL-SIGNAL
THUS THE VOLTAGE GAIN: $A_v = \frac{v_{out}}{v_{in}} = -g_m R_L$

THIS EQUATION CAN BE INTERPRETED BY A CIRCUIT:

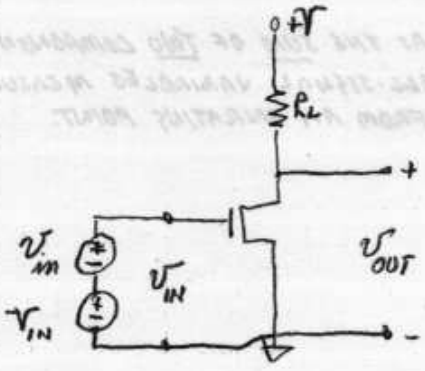


ANOTHER DEPENDENT SOURCE, THIS ONE LINEAR.

THIS IS THE INCREMENTAL OR SMALL SIGNAL MODEL

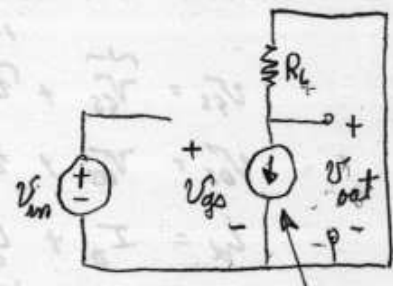
FOR THE AMPLIFIER CIRCUIT. IT CAN BE DRAWN

DIRECTLY FROM THE ORIGINAL CIRCUIT DIAGRAM:



REPLACE FET BY $g_m v_{gs}$ DEPENDENT SOURCE

SET ALL FIXED SOURCES TO ZERO



IDENTICAL TO CIRCUIT ABOVE

$$g_m v_{gs} = g_m v_{in}$$

OBVIOUSLY THE TRANSCONDANCE g_m IS DEPENDENT ON THE OPERATING POINT:

$$g_m = K(V_{SS} - V_T) \quad (V_{SS} - V_T) = \sqrt{\frac{2I_D}{K}} \quad \text{INVERTING THE EQUATION WE STARTED WITH.}$$

OPER. POINT DEPENDENCE
 $(V_{SS} = V_{IN})$

THUS $g_m = \sqrt{2KI_D}$ THE USUAL WAY OF EXPRESSING OPERATING POINT DEPENDENCE

IN TERMS OF V_{gs} , HOW SMALL IS SMALL ENOUGH? FROM EQ. (2)

$$\frac{\text{SECOND-ORDER TERM}}{\text{LINEAR TERM}} = \frac{\frac{K}{2} V_{gs}^2}{K(V_{SS} - V_T) V_{gs}} = \frac{V_{gs}}{2(V_{SS} - V_T)}$$

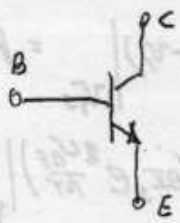
IF $V_{gs} \ll 2(V_{SS} - V_T)$ OR $V_{gs} \ll \frac{2g_m}{K}$ THE LINEAR APPROXIMATION IS OK.

DEMO

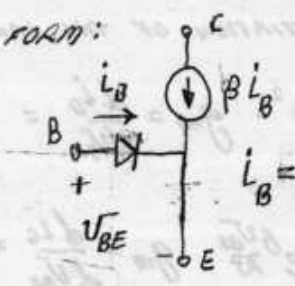
(BJT)

A SECOND EXAMPLE A BIPOLAR JUNCTION TRANSISTOR, WHICH IS IN A DIFFERENT CLASS OF SEMICONDUCTOR DEVICES HAS A LARGE-SIGNAL

MODEL OF THIS FORM:

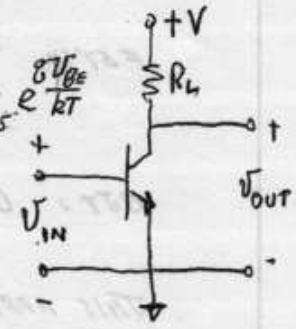


CIRCUIT SYMBOL



LARGE SIGNAL MODEL

$$i_B = I_S (e^{qV_{BE}/kT} - 1) \approx I_S e^{qV_{BE}/kT}$$



CIRCUIT

AND IS USED IN A CIRCUIT LIKE THIS

USE TAYLOR SERIES TO EXPAND i_B AND LINEARIZE THE $i_B = f(V_{BE})$ RELATIONSHIP:

$$I_B + i_b = f(V_{BE}) \Big|_{V_{BE}} + \frac{1}{1!} \frac{df}{dV_{BE}} \Big|_{V_{BE}} V_{be} + \frac{1}{2!} \frac{d^2f}{dV_{BE}^2} \Big|_{V_{BE}} V_{be}^2 + \dots$$

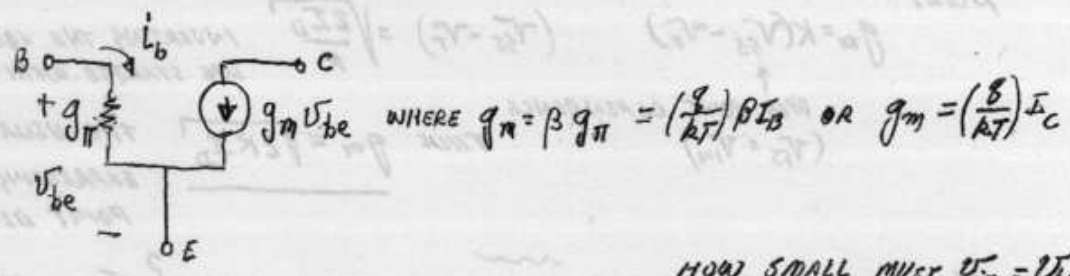
HIGHER ORDER TERMS

$$= \underbrace{I_S e^{\frac{qV_{BE}}{kT}}}_{I_B} + \underbrace{\frac{q}{kT} (I_S e^{\frac{qV_{BE}}{kT}})}_{I_b} V_{be} + \underbrace{\frac{1}{2} \left(\frac{q}{kT}\right)^2 (I_S e^{\frac{qV_{BE}}{kT}})}_{\text{NEGLECT}} V_{be}^2 + \dots$$

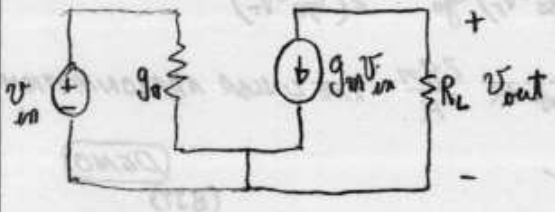
$$I_b = \frac{q}{kT} I_B V_{be}$$

$$= g_m V_{be} \quad \text{WHERE } g_m = \left(\frac{q}{kT} I_B\right)$$

THE INCREMENTAL BJT MODEL IS:



AND THE INCREMENTAL CIRCUIT IS:



HOW SMALL MUST $v_{in} = V_{be}$ BE TO PERMIT THE LINEAR APPROXIMATION?

$$\frac{\text{SECOND ORDER TERM}}{\text{FIRST ORDER TERM}} = \left(\frac{q}{kT}\right) \frac{V_{be}}{2}$$

$$\text{CONDITION: } V_{be} \ll \frac{q}{2kT}$$

IN EACH OF THESE CASES THE INCREMENTAL PARAMETERS CAN BE OBTAINED BY DIFFERENTIATION OF THE LARGE SIGNAL MODEL.

$$\text{FET: } I_D = \frac{K}{2} (V_{GS} - V_T)^2 \quad g_m = \frac{dI_D}{dV_{GS}} = K(V_{GS} - V_T) \Big|_{V_{GS}} = \sqrt{2KI_D}$$

$$\text{BJT: } I_C = \beta I_B = \beta I_S e^{\frac{qV_{BE}}{kT}} \quad g_m = \frac{dI_C}{dV_{BE}} = \left(\frac{q}{kT}\right) (\beta I_S e^{\frac{qV_{BE}}{kT}}) \Big|_{V_{BE}} = \left(\frac{q}{kT}\right) I_C$$

THIS APPROACH GIVES NO GUIDANCE ON THE LIMITS OF LINEARITY.