

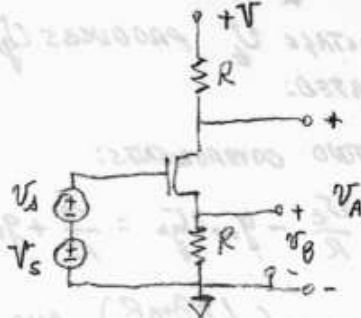
NOTES FOR 6.002 LECTURE # 9 THURSDAY, MARCH 6, 2003

READ: 8.2.4

ERROR ON LECTURE 8 NOTES: PAGE 4, THE CONDITION FOR LINEARITY SHOULD READ: $V_{be} \ll \frac{2kT}{q}$ HAD I CHECKED DIMENSIONS WOULD HAVE BEEN OK!

MORE EXAMPLES OF SMALL-SIGNAL ANALYSIS:

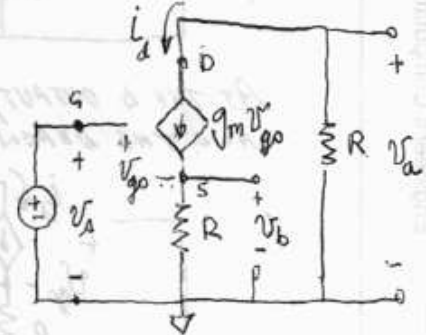
PHASE-SPLITTER



CIRCUIT DIAGRAM

THE FIXED SOURCE V_s ESTABLISHES AN OPERATING POINT. THE SIGNAL SOURCE IS v_a

THERE ARE TWO OUTPUTS



SMALL-SIGNAL MODEL

NOTE USE OF \diamond TO DISTINGUISH THE DEPENDENT CURRENT SOURCE,

AND NOTE THAT IN THIS CIRCUIT $v_d \neq v_{gs}$. RATHER $v_d = v_{gs} + R i_d$

WHERE $R i_d$ IS THE DROP ACROSS THE RESISTOR AT THE SOURCE TERMINAL OF THE FET. THE RELEVANT EQUATIONS ARE:

$$\left. \begin{aligned} v_{gs} &= v_d - R i_d \\ i_d &= g_m v_{gs} \end{aligned} \right\} \text{ SOLUTION: } v_{gs} = \frac{v_d}{1 + g_m R}$$

FROM WHICH THE OUTPUT VOLTAGES CAN BE WRITTEN:

$$\left. \begin{aligned} v_b &= i_d R \\ v_a &= -i_d R \end{aligned} \right\} \text{ OR: } \textcircled{1} \left. \begin{aligned} v_b &= v_d \frac{g_m R}{1 + g_m R} \\ v_a &= -v_d \frac{g_m R}{1 + g_m R} \end{aligned} \right\}$$

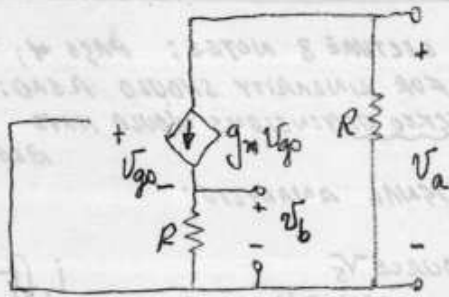
OUTPUTS ARE EQUAL IN MAGNITUDE AND OPPOSITE IN SIGN. THUS THE NAME "PHASE-SPLITTER"

IF $g_m R \gg 1$, WHICH IS USUALLY THE CASE: $\left\{ \begin{aligned} v_b &\approx v_d \\ v_a &\approx -v_d \end{aligned} \right.$ INDEPENDENT OF g_m !

A FIRST ILLUSTRATION OF THE CONCEPT OF NEGATIVE FEEDBACK.

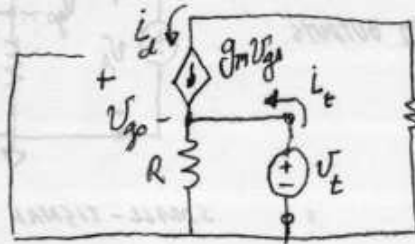
THE EQUATIONS $\textcircled{1}$ DESCRIBE THE OPEN-CIRCUIT VOLTAGES AT THE OUTPUTS. TO CREATE THEVENIN EQUIVALENT MODELS AT THE TWO OUTPUTS, EITHER SHORT-CIRCUIT CURRENTS OR THEVENIN EQUIVALENT RESISTANCES MUST BE DETERMINED. TO DO THE LATTER (R_{th}) TURN OFF THE INDEPENDENT SOURCE IN THE SMALL-SIGNAL MODEL. THAT IS, SET $v_d = 0$. IN TERMS OF THE DETERMINATION OF R_{th} , WHERE A TEST SOURCE WILL BE APPLIED, v_a IS AN INDEPENDENT SOURCE

RESULTING SMALL SIGNAL MODEL:



CONSIDER THE a OUTPUT FIRST.
 APPLY A VOLTAGE v_t ACROSS THE
 TERMINAL. IT DOES NOT AFFECT
 THE DEPENDENT GENERATOR AND
 THE CURRENT IS JUST v_t/R
 AND $R_{TH}|_a$ AT THE a OUTPUT IS R .

AT THE b OUTPUT, APPLICATION OF A TEST VOLTAGE v_t PRODUCES $v_{gs} = -v_t$
 AND THE DEPENDENT GENERATOR IS ACTIVATED:



i_t HAS TWO COMPONENTS:

$$i_t = \frac{v_t}{R} - g_m v_{gs} = \frac{v_t}{R} + g_m v_t$$

THUS $i_t = v_t \left(\frac{1+g_m R}{R} \right)$ AND

THE THEVENIN RESISTANCE IS $R_{TH}|_b = \frac{v_t}{i_t} = \frac{R}{1+g_m R}$

HAD WE CHOSEN INSTEAD TO EVALUATE THE SHORT-CIRCUIT CURRENTS:

AT a OUTPUT, NOW WITH v_a ACTING

$$I_{sc}|_a = -i_d = -\frac{v_a g_m}{1+g_m R} \quad \text{FROM EQUATIONS 1}$$

AT b OUTPUT, ALSO WITH v_b ACTING:

$$I_{sc}|_b = g_m v_b \quad \text{BY INSPECTION}$$

AS A CHECK, FORM $R_{TH} = \frac{V_{oc}}{I_{sc}}$:

AT a $R_{TH}|_a = \frac{-v_a g_m R / (1+g_m R)}{-v_a g_m / (1+g_m R)} = R \quad \text{OK}$

AT b $R_{TH}|_b = \frac{+v_b g_m R / (1+g_m R)}{g_m v_b} = \frac{R}{1+g_m R} \quad \text{OK}$

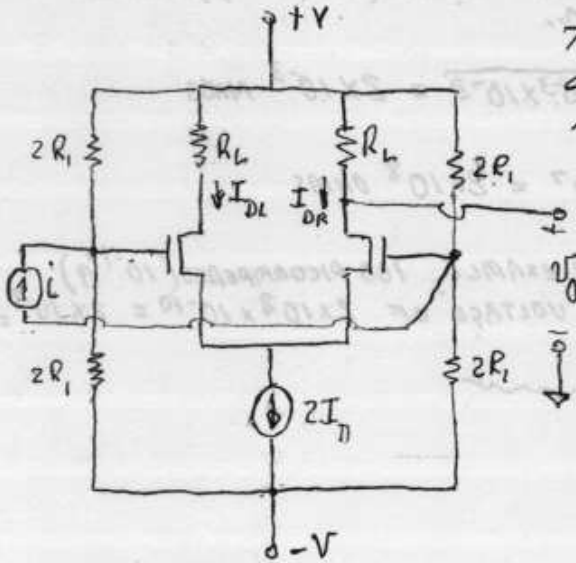
NOTE THAT $R_{TH}|_a \ll R_{TH}|_b$

WHICH IS A CONSEQUENCE OF THE NEGATIVE FEEDBACK.

NOTE: IN EVALUATING R_{TH} WITH A TEST SIGNAL AT THE TERMINALS THE DEPENDENT SOURCE MUST BE RETAINED.

IN EVALUATING V_{oc} AND I_{sc} THE SIGNAL SOURCE MUST BE RETAINED, AND THE DEPENDENT SOURCE AS WELL. OTHERWISE V_{oc} AND I_{sc} WOULD BE ZERO.

A DIFFERENTIAL AMPLIFIER
SECOND EXAMPLE: USEFUL FOR MEASURING MINISCULE CURRENTS:



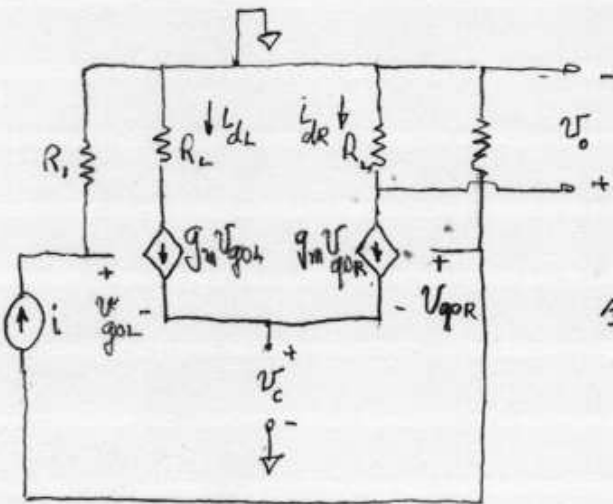
THE CIRCUIT IS SYMMETRICAL AROUND THE CENTER LINE AND THE COMPONENTS, INCLUDING FETS ARE MIRROR IMAGES

THE CURRENT SOURCE $2I_D$ SPLITS EQUALLY SO THAT $I_{DL} = I_{DR} = I_D$

CONSIDER FUNCTION QUANTITATIVELY FIRST

THE VOLTAGES AT THE GATES ARE ZERO WHEN $I_i = 0$ (THINK VOLTAGE DIVIDERS)

SET INDEPENDENT SOURCES TO ZERO, REPLACE FETS WITH SMALL-SIGNAL MODELS
 LET v_o BE THE SMALL-SIGNAL COMPONENT OF V_o . IDENTIFY THE SMALL-SIGNAL SOURCE TO GROUND VOLTAGE AS v_c .



EQUATIONS:

$$V_{g0L} = iR_i - v_c$$

$$V_{g0R} = -iR_i - v_c$$

$$v_o = -i_{dR} R_L$$

AND $i_{dL} = -i_{dR}$ FORCED BY THE CIRCUIT CONFIGURATION

SOLUTION $\left. \begin{aligned} i_{dL} &= g_m V_{g0L} \\ i_{dR} &= g_m V_{g0R} \end{aligned} \right\} \rightarrow g_m (iR_i - v_c) = -g_m (-iR_i - v_c)$
 ONLY SOLUTION IS $v_c = 0$

$$v_o = -i_{dR} R_L = -g_m R_L V_{g0R} = (g_m R_L R_i) i$$

THE TRANSRESISTANCE OF THE AMPLIFIER IS $\frac{v_o}{i} = g_m R_L R_i$

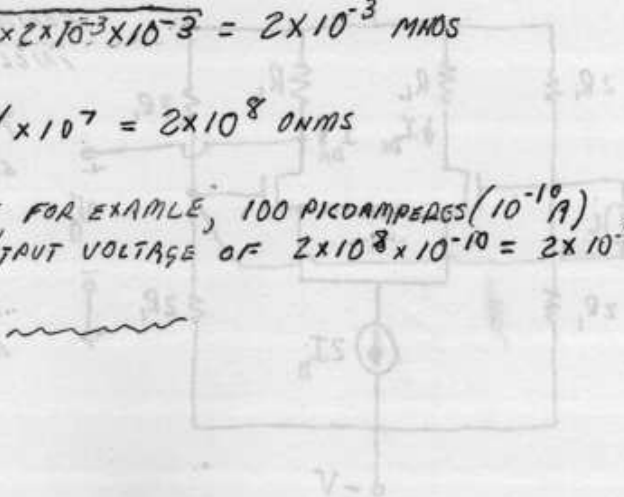
(A CURRENT INPUT PRODUCES A VOLTAGE OUTPUT)

SUPPOSE THE FETs ARE BIASED AT 1 mA (1×10^{-3}), THAT $K = 2 \frac{mA}{V^2}$, THAT $R_1 = 10^7 \Omega$ AND $R_L = 10^4 \Omega$.

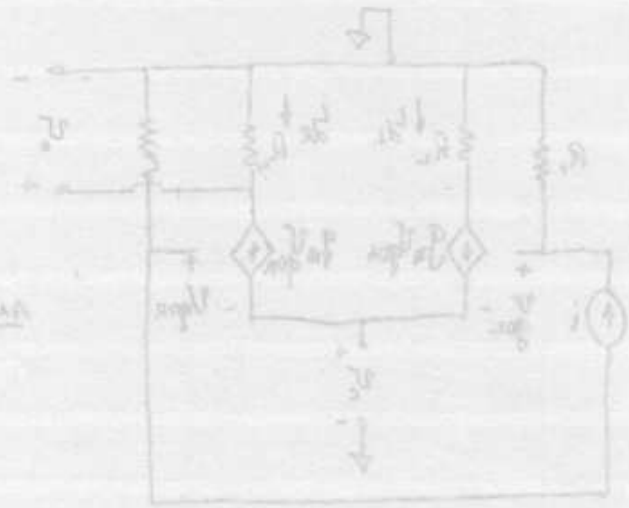
$$g_m = \sqrt{2KI_D} = \sqrt{2 \times 2 \times 10^{-3} \times 10^{-3}} = 2 \times 10^{-3} \text{ MMOS}$$

$$\frac{V_o}{I} = 2 \times 10^{-3} \times 10^4 \times 10^7 = 2 \times 10^8 \text{ ONMS}$$

THUS A CURRENT I OF FOR EXAMPLE, 100 PICDAMPERES ($10^{-10} A$) WOULD PRODUCE AN OUTPUT VOLTAGE OF $2 \times 10^8 \times 10^{-10} = 2 \times 10^{-2} = 20 \text{ mV}$



Equations:
 $V_o = I R_L$
 $V_o = -I R_L$
 $I = -\frac{V_o}{R_L}$
 $V_o = -g_m V_i R_L$
 $V_o = -g_m V_i R_L$



Solution: $V_o = -g_m V_i R_L$
 $V_o = -g_m V_i R_L$
 $V_o = -g_m V_i R_L$

$V_o = -g_m V_i R_L = -g_m V_i R_L$
 THE TRANSMITTANCE OF THE AMPLIFIER IS $\frac{V_o}{I} = g_m R_L R_L$
 (A CURRENT INPUT PRODUCES A VOLTAGE OUTPUT)