

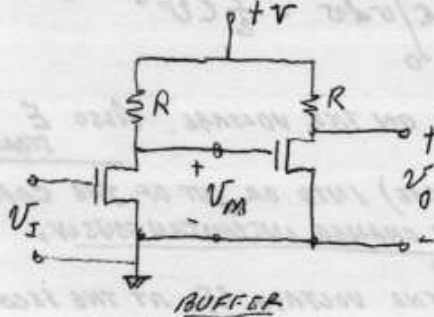
NOTES FOR 6.002 LECTURE #10, MARCH 11, 2003

READ: 9.1-9.3

LAB #2 NEXT WEEK!

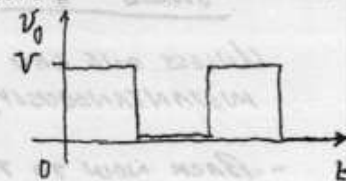
UP TO NOW WE HAVE EXPLORED CIRCUITS CONTAINING, IN ADDITION TO SOURCES (INDEPENDENT AND DEPENDENT) ONLY RESISTORS. NOW CONSIDER ELEMENTS WHICH STORE ENERGY: CAPACITORS AND INDUCTORS.

BEGIN WITH A DEMO. INVOLVING TWO INVERTING GATES:



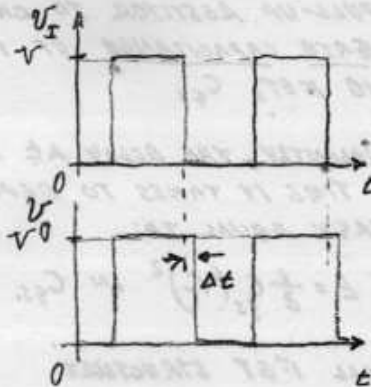
V_I	V_O
0V	0V
V	V

IF DRIVEN WITH A SQUARE WAVE WHICH LOOKS LIKE:



THE OUTPUT SHOULD REPLICATE THIS

AT LOW PRF (PULSE REPRITITION FREQUENCY) THE OUTPUT INDEED REPLICATES THE INPUT. AT HIGHER PRF (AND SHORTER PULSES) IT LOOKS LIKE THIS:



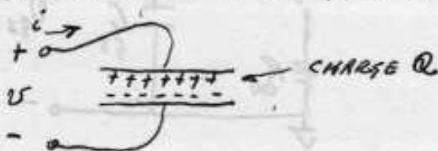
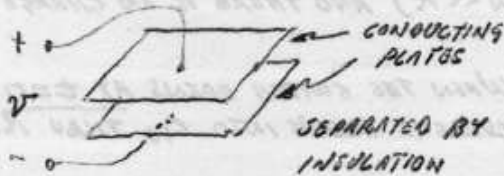
THE TRANSITION FROM V TO ZERO IS DELAYED A BIT, WHICH HAS THE EFFECT OF WIDENING THE OUTPUT PULSE.

THIS DELAY IS A MANIFESTATION OF ENERGY STORAGE IN THE OXIDE LAYER OF THE SECOND FET.

TWO CIRCUIT ELEMENTS STORE ENERGY: CAPACITORS AND INDUCTORS.

FOCUS TODAY ON CAPACITORS WHERE THE ENERGY IS STORED IN AN ELECTRIC FIELD.

PARALLEL PLATE CAPACITOR: A VOLTAGE V APPLIED ACROSS THE PLATES ESTABLISHES LAYERS OF EQUAL BUT OPPOSITE CHARGE ON THE PLATES. FOR V > 0



DEFINING RELATIONSHIP: $Q = CV$ WHERE C IS THE CAPACITANCE IN FARADS

OBVIOUSLY THE TERMINAL CURRENT i IS DETERMINED BY THE RATE OF CHANGE OF CHARGE:

$$i = \frac{dQ}{dt} = \frac{d}{dt}(CV) = C \frac{dV}{dt} \quad i = C \frac{dV}{dt} \text{ IS THE CONSTITUTENT RELATIONSHIP FOR THE CAPACITOR}$$

CONSIDER AN UNCHARGED CAPACITOR ($V=0$). IF THE VOLTAGE IS INCREASED TO A VALUE V , THE ENERGY STORED IN THE CAPACITOR IS:

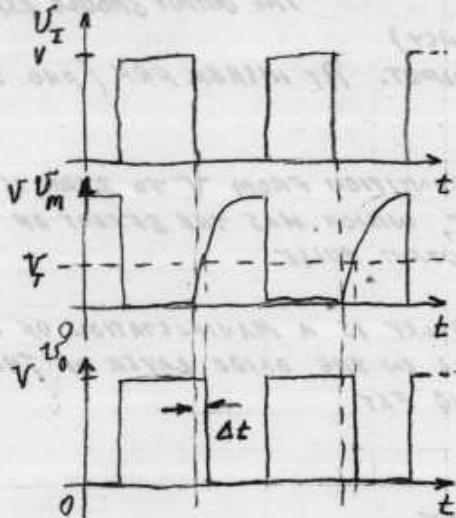
$$E = \int_{t_1}^{t_2} i \cdot V dt \quad \text{WHERE } i \cdot V \text{ IS BY DEFINITION THE INSTANTANEOUS POWER INTO } C.$$

$$E = \int_{t_1}^{t_2} C V \frac{dV}{dt} dt = C \int_0^V V dV = \frac{1}{2} CV^2$$

$E_{\text{STORED}} = \frac{1}{2} CV^2$ DEPENDENT ONLY ON THE VOLTAGE. ALSO $E_{\text{STORED}} = \frac{1}{2} \frac{Q^2}{C}$

UNLESS ONE CAN MOVE ENERGY (OR CHARGE) INTO OR OUT OF THE CAPACITOR INSTANTANEOUSLY, THE VOLTAGE CANNOT CHANGE INSTANTANEOUSLY.

BACK NOW TO THE DEMO. LOOK AT THE VOLTAGE V_M AT THE SECOND GATE.



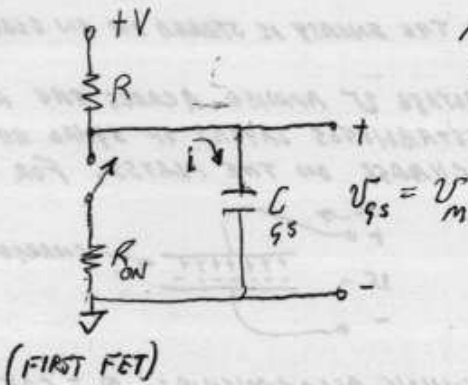
THE VOLTAGE V_M DOES NOT RISE INSTANTANEOUSLY BECAUSE IT TAKES TIME FOR THE CURRENT THROUGH THE PULL-UP RESISTOR TO CHARGE UP THE GATE CAPACITANCE OF THE SECOND FET, C_{gs}

EQUIVALENTLY, THE DELAY Δt REFLECTS THE TIME IT TAKES TO DEPOSIT ENERGY EQUAL TO:

$$E = \frac{1}{2} C_{gs} (V_T)^2 \text{ IN } C_{gs}.$$

RECALL I-FET STRUCTURE

ANALYSIS OF THE TRANSITION:



INITIALLY THE SWITCH IS CLOSED, THE VOLTAGE V_M IS APPROXIMATELY ZERO (ASSUMING $R_{ON} \ll R$) AND THERE IS NO CHARGE ON C_{gs}

WHEN THE SWITCH OPENS AT $t=0$ CURRENT FLOWS INTO C_{gs} THRU R .

A NODE EQUATION AT THE DRAIN OF THE FIRST (LEFT) FET FOR $t > 0$ IS:
(TAKING CURRENTS OUT OF THE NODE)

$$\frac{v_m - V}{R} + C_{gs} \frac{dv_m}{dt} = 0 \quad \text{OR} \quad RC_{gs} \frac{dv_m}{dt} + v_m = V$$

FIRST-ORDER
LINEAR
ORDINARY
CONSTANT
COEFFICIENT
DIFF. EQ.

IN GENERAL, SOLUTION IS $v_m(t) = v_{mh}(t) + v_{mp}(t)$

HOM. SOLUTION FITS HOM. EQ;

HOMOGENEOUS SOLUTION PARTICULAR SOLUTION

$$\tau \frac{dv_m}{dt} + v_m = 0 \quad \text{AND IS} \quad v_m(t) = A e^{-t/\tau}$$

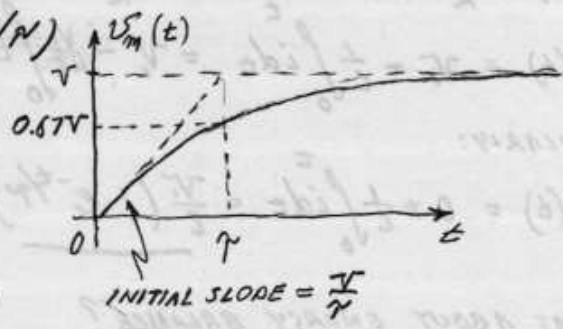
WHERE A IS A
CONSTANT DETERMINED
BY CONDITIONS AT $t=0$

WHERE $\tau = RC_{gs}$ IS THE TIME CONSTANT

THE PARTICULAR SOLUTION IS $v_{mp}(t) = V$ BY INSPECTION.

THUS $v_m(t) = V + A e^{-t/\tau}$ AT $t=0$, $v_m(t) = 0$ THUS $A = -V$

$$v_m(t) = V(1 - e^{-t/\tau})$$



TO DETERMINE Δt

$$\frac{v_T}{V} = 1 - e^{-\Delta t/\tau}$$

$$\text{OR } \Delta t = -\tau \ln(1 - \frac{v_T}{V})$$

NOTE EVIDENCE
OF TURN-ON
OF SECOND FET
IN DEMO

WHY?

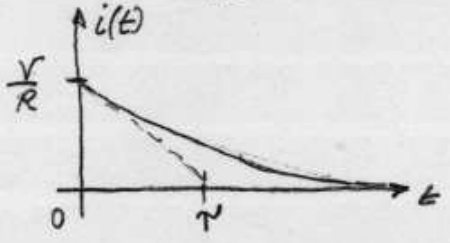
FROM THE DEMO: $\tau \approx 15 \mu\text{SEC}$, $R = 10^5 \Omega$ $C_{gs} = \frac{\tau}{R} = 150 \times 10^{-12} \text{ F}$ OR 150 pF

SPEC SHEET SAYS C_{gs} IS 26-40 pF. DIFFERENCE IS WIRING CAPACITANCE IN THE DEMO.

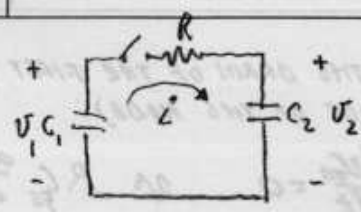
WHY IS THERE NO SIMILAR DELAY WHEN THE SWITCH CLOSSES?

THE SOLUTION ABOVE CAN BE USED TO DETERMINE THE CHARGING CURRENT:

$$i(t) = C_{gs} \frac{dv_m}{dt} = C_{gs} \frac{V}{\tau} e^{-t/\tau} = \frac{V}{R} e^{-t/\tau}$$



A SECOND EXAMPLE:



FOR $t < 0$ THE SWITCH IS OPEN
 THE CAPACITOR C_1 IS CHARGED TO
 $V_1 = V_0$ VOLTS AND THERE IS
 NO CHARGE ON C_2 , I.E., $V_2 = 0$
 AT $t = 0$ THE SWITCH CLOSSES

COULD WRITE TWO NODE EQUATIONS, IT IS EASIER TO WRITE KVL AROUND THE LOOP

$$+ \frac{1}{C_1} \int i dt + iR + \frac{1}{C_2} \int i dt = 0 \quad \text{DIFFERENTIATE: } \left(\frac{1}{C_1} + \frac{1}{C_2}\right) i + R \frac{di}{dt} = 0$$

TO SIMPLIFY ALGEBRA LET $C_1 = C_2 = C \rightarrow \frac{RC}{2} \frac{di}{dt} + i = 0 \quad \frac{RC}{2} = \tau$

SOLUTION: $i(t) = A e^{-t/\tau}$ WHERE A IS A CONSTANT TO BE DETERMINED

AT $t = 0^+$ THE INITIAL VOLTAGE ON C_1 APPEARS ACROSS R BECAUSE
 THE CHARGES ON C_1, C_2 (AND THE STORED ENERGIES) CANNOT CHANGE
 INSTANTANEOUSLY.

$$i(0^+) = \frac{V_1}{R} = A \quad \text{AND} \quad i(t) = \frac{V_1}{R} e^{-t/\tau}$$

$$V_1(t) = V_1 - \frac{1}{C} \int_0^t i dt = V_1 - \frac{V_1}{RC} \int_0^t e^{-t/\tau} dt = V_1 - \frac{V_1}{2} (1 - e^{-t/\tau})$$

SIMILARLY:

$$V_2(t) = 0 + \frac{1}{C} \int_0^t i dt = \frac{V_1}{2} (1 - e^{-t/\tau})$$

WHAT ABOUT ENERGY BALANCE?

$$t < 0 \quad \text{STORED ENERGY} = \frac{1}{2} C V_1^2$$

$$t \rightarrow \infty \quad \text{STORED ENERGY} = \frac{1}{2} C \left(\frac{V_1}{2}\right)^2 + \frac{1}{2} C \left(\frac{V_1}{2}\right)^2 = \frac{1}{4} C V_1^2$$

WHAT HAPPENED TO THE OTHER HALF OF THE INITIAL STORED ENERGY?

