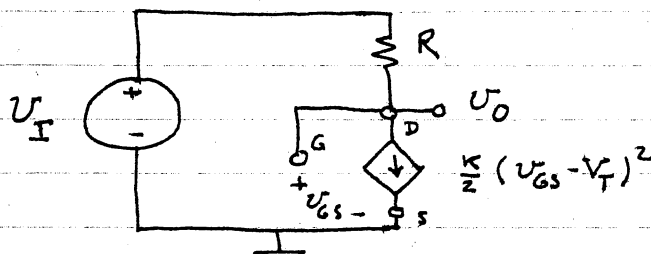


Massachusetts Institute of Technology
Department of Electrical Engineering and Computer Science

6.002 Electronic Circuits
Fall 2000

Quiz 2 Solutions

1. Replace MOSFET by its large-signal model valid in the saturation regime:



A) From the circuit, $V_O = U_I - \frac{\kappa R}{2} (V_{GS} - V_T)^2$

but $V_{GS} = V_O$, so $V_O = U_I - \frac{\kappa R}{2} (V_O - V_T)^2$

Plugging the numbers in: $2.5 = 5 - \frac{2 \cdot R (\text{k}\Omega)}{2} (2.5 - 1)^2$

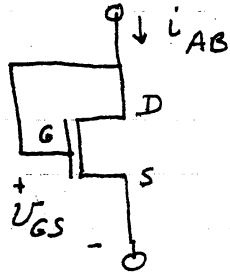
$$\Rightarrow R = \frac{2.5}{(1.5)^2} = 1.11 \text{ k}\Omega$$

B) Requirements for saturation are $V_{GS} > V_T$ and $V_{DS} > V_{GS} - V_T$.

$$V_{GS} = V_O = 2.5 \text{ V} > V_T = 1 \text{ V} \quad \text{o.k.}$$

$$V_{DS} = V_O = 2.5 \text{ V} > V_{GS} - V_T = 1.5 \text{ V} \quad \text{o.k.}$$

2.



In the saturation regime, the large signal model is

$$i_{AB} = \frac{\kappa}{2} (V_{GS} - V_T)^2$$

Let $V_{GS} = V_{GS} + v_{gs}$ and expand:

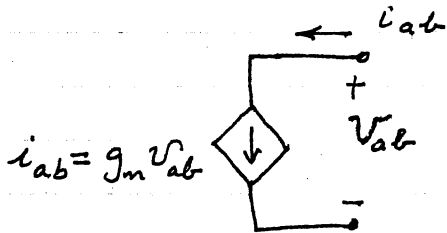
$$\begin{aligned} i_{AB} &= \frac{\kappa}{2} (V_{GS} + v_{gs} - V_T)^2 \\ &= \frac{\kappa}{2} (V_{GS} - V_T)^2 + \kappa (V_{GS} - V_T) v_{gs} + \frac{\kappa}{2} v_{gs}^2 \end{aligned}$$

neglect
↑
1

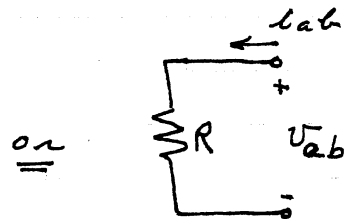
Writing $i_{AB} = I_{AB} + i_{ab}$ and equating bias and small-signal terms:

$$I_{AB} = \frac{\kappa}{2} (V_{GS} - V_T)^2, \quad i_{ab} = \kappa (V_{GS} - V_T) v_{gs}$$

from which we can draw the following models:

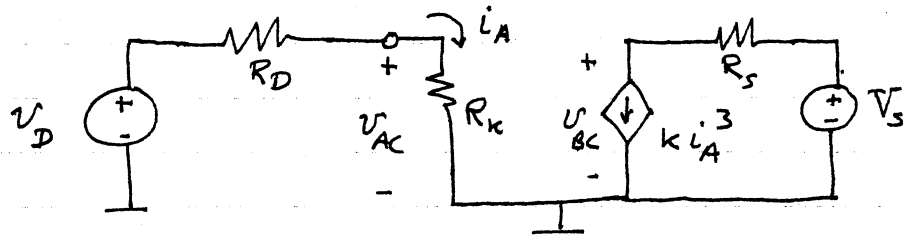


$$g_m = \kappa (V_{AB} - V_T)$$



$$R = \frac{1}{g_m} = \frac{1}{\kappa (V_{AB} - V_T)}$$

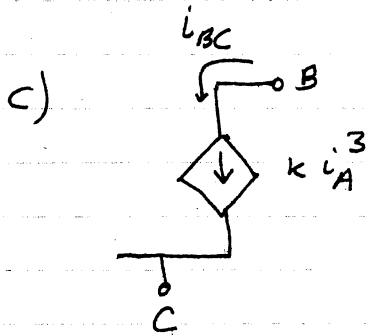
3. A) Substituting the model into the FRAMPLIFIER:



B)

$$V_0 = V_{BC} = V_S - k R_S I_A^3$$

$$V_0 = V_S - k R_S \left(\frac{V_D}{R_D + R_K} \right)^3$$



Let $i_A = I_A + i_a$

Then $i_{BC} = k(I_A + i_a)^3$

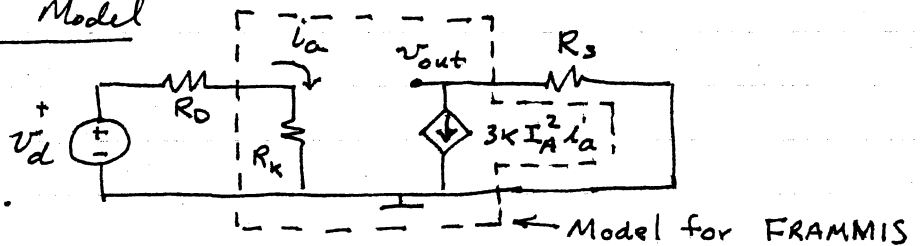
$$= k I_A^3 + 3k I_A^2 i_a + \dots$$

neglect!

Or, with $i_{BC} = I_{BC} + i_{bc}$

$$i_{bc} = 3k I_A^2 i_a$$

Incremental Model



Model (2) is clearly the correct choice.

D) From the above model

$$V_0 = -3k R_S I_A^2 \frac{V_d}{R_D + R_K} \Rightarrow A = - \frac{3k R_S I_A^2}{R_D + R_K}$$

Note: $I_A = \frac{V_D}{R_D + R_K}$

4. A) Saturation discipline requires $v_{DS} > v_{GS} - V_T$

In this problem, $v_C > v_I - V_T$

B) From the i - v relationship of the capacitor

$$C \frac{dv_C}{dt} = -i_{DS} = -\frac{\mu}{2} (v_I - V_T)^2$$

which is valid for $v_I > V_T$. For $v_I < V_T$, $i_{DS} = 0$

and the differential eq is $C \frac{dv_C}{dt} = 0$.

C) For $0 < t < 1 \mu s$, $v_I = 0$, so $\frac{dv_C}{dt} = 0$ and

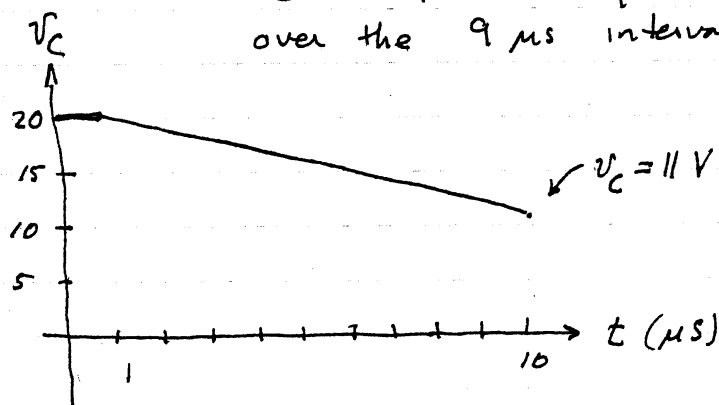
$$v_C = \text{const.} = v_C(0) = 20 \text{ V.}$$

For $1 < t < 10 \mu s$

$$10^{-7} \frac{dv_C}{dt} = -\frac{2 \times 10^{-3}}{2} (10)^2 \quad (\text{units!})$$

$$\frac{dv_C}{dt} = -10^6 \frac{\text{V}}{\text{s}} \Rightarrow v_C = 20 - 10^6 (t - 10^{-6})$$

v_C decays linearly from 20 V to 11 V over the 9 μs interval.



5. A) When switch closes, v_1 decays from its initial value to 0 with time constant $RC = 10^5 \times 10^{-4} = 10$ sec

Taking $t=0$ as time switch closes, $v_1 = 1000 e^{-t/10}$

which reaches 50 when $50 = 1000 e^{-t_1/10}$

$$\text{or } t_1 = 10 \ln 20 \approx 30 \text{ sec.}$$

B) Writing a KVL around the loop

$$Ri + v_2 - v_1 = 0$$

Again taking $t=0$ as the time the switch closes,

$$v_2 = \frac{1}{C_2} \int_0^t i dt'$$

$$v_1 = 1000 - \frac{1}{C_1} \int_0^t i dt'$$

so

$$Ri + \frac{1}{C_2} \int_0^t i dt' + \frac{1}{C_1} \int_0^t i dt' = 1000$$

and, differentiating,

$$\Rightarrow R \frac{di}{dt} + \frac{1}{C_{eq}} i = 0, \quad C_{eq} = \frac{C_1 C_2}{C_1 + C_2}$$

$$C) \quad RC_{eq} = R \frac{C_1 C_2}{C_1 + C_2} = R \cdot \frac{C_1^2/2}{3C_1/2} = \frac{RC_1}{3} = 3.33 \text{ sec.}$$

$$D) \quad \text{At } t=0, \quad v_2 = 0, \quad v_1 = 1000 \quad \text{so } i = \frac{1000}{10^5} = 10^{-2} \text{ A.}$$

$$E) \quad \text{From B, } v_1 = 1000 - 10^4 \int_0^t i(t') dt'$$

while from C) and D) $i = 10^{-2} e^{-t/3.33}$ A.

$$\text{so } v_1 = 1000 - 10^2 \times 3.33 (1 - e^{-t/3.33}) \Rightarrow 667 \text{ V at } t = \infty \quad \underline{\underline{\text{No Good!}}}$$