

INTRODUCTION TO EECS II

### DIGITAL COMMUNICATION SYSTEMS

### 6.02 Spring 2012 Lecture #7

- Viterbi decoding of convolutional codes
   Path and branch metrics
   Hard-decision & soft-decision decoding
- Performance issues: decoder complexity, post-decoding BER, "free distance" concept

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Lecture 7, Slide #1

### Example Msg Codeword Received Hamming distance 0000 00000000000 7 0001 000000111110 8 · For the code p0 = x[n]+x[n-1]+x[n-2]0010 000011111000 8 p1 = x[n] + x[n-1]0011 000011010110 4 Received: 001111100000 6 111011000110 0101 001111011110 5 Some errors have 0110 001101001000 7 occurred... 0111 001100100110 6 111011000110 What's the 4-bit 1000 111110000000 4 message? 111110111110 5 · Look for message 111101111000 whose codeword is 2 111101000110 1011 closest to rcvd bits 1100 110001100000 5 1101 110001011110 4 Most likely: 1011 110010011000 6 1111 110010100110 Initial state: 00 Lecture 7, Slide #3

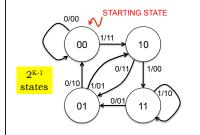
# **Encoding & Decoding Convolutional Codes**

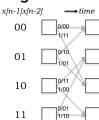
- · Transmitter (aka Encoder)
  - Beginning at starting state, processes message bit-by-bit
  - For each message bit: makes a state transition, sends  $p_0p_1$ ...
  - Pad message with K-1 zeros to ensure return to starting state
- · Receiver (aka Decoder)
  - Doesn't have direct knowledge of transmitter's state transitions;
     only knows (possibly corrupted) received parity bits, p;
  - Must find most likely sequence of transmitter states that could have generated the received parity bits, p<sub>i</sub>
  - If BER < 1/2, P(more errors) < P(fewer errors)
  - When BER < ½, maximum-likelihood message sequence is the one that generated the codeword (here, sequence of parity bits) with the smallest Hamming distance from the received codeword (here, parity bits)
  - I.e., find nearest valid codeword *closest* to the received codeword - Maximum-likelihood (ML) decoding

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# Key Concept for Decoding: A Trellis

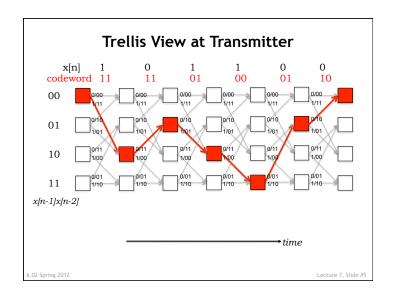




- Example: *K*=3, rate-½ convolutional code
  - $G_0 = 111: p_0 = 1*x[n] + 1*x[n-1] + 1*x[n-2]$
  - $-G_1 = 110$ :  $p_1 = 1*x[n] + 1*x[n-1] + 0*x[n-2]$
- States labeled with x/n-1 | x/n-2|
- Arcs labeled with  $x/n/p_0p_1$

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# Viterbi Algorithm

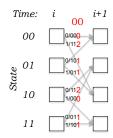
- · Want: Most likely message sequence
- · Have: (possibly corrupted) received parity sequences
- · Viterbi algorithm for a given K and r:
  - Works incrementally to compute most likely message sequence
  - Uses two metrics
- Branch metric: BM(xmit,rcvd) proportional to likelihood that transmitter sent *xmit* given that we've received *rcvd*.
  - "Hard decision": use digitized bits, compute Hamming distance between xmit and rcvd. Smaller distance is more likely if BER < 1/2
  - "Soft decision": use function of received voltages directly
- Path metric: PM[s,i] for each state s of the 2<sup>K-1</sup> transmitter states and bit time i where 0 ≤ i < len(message).
  - PM[s,i] = most likely sum of  $BM(xmit_m,received parity)$  over all message sequences m that place transmitter in state s at time i
  - PM[s,i+1] computed from PM[s,i] and p<sub>0</sub>[i],...,p<sub>r-1</sub>[i]

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### Hard-decision Branch Metric

- BM = Hamming distance between expected parity bits and received parity bits
- Compute BM for each transition arc in trellis
  - Example: received parity = 00
  - $\begin{array}{ll} & \mathrm{BM}(00,00) = 0 \\ & \mathrm{BM}(01,00) = 1 \end{array}$
  - BM(10,00) = 1BM(11,00) = 2
- Will be used in computing PM[s,i+1] from PM[s,i].
- We want to use the most likely BM, which, means minimum BM.



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### Computing PM[s,i+1]

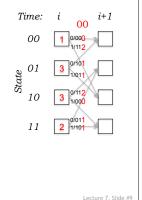
Starting point: we've computed PM[s,i], shown graphically as label in trellis box for each state at time *i*.

Example: PM[00,i] = 1 means there was 1 bit error detected when comparing received parity bits to what would have been transmitted when sending the most likely message, considering all messages that place the transmitter in state 00 at time i.

Q: What's the most likely state s for the transmitter at time *i*?

A: state 00 (smallest PM[s,i])

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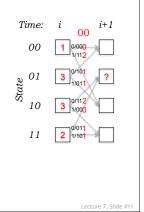
# Computing PM[s,i+1] cont'd.

Example cont' d: to arrive in state 01 at time i+1, either

1)The transmitter was in state 10 at time i and the i<sup>th</sup> message bit was a 0. If that's the case, the transmitter sent 11 as the parity bits and there were 2 bit errors since we received 00. Total bit errors = PM[10,i] + 2 = 5 *OR* 

2)The transmitter was in state 11 at time i and the i<sup>th</sup> message bit was a 0. If that's the case, the transmitter sent 01 as the parity bits and there was 1 bit error since we received 00. Total bit errors = PM[11,i] + 1 = 3 Which is more likely?

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# Computing PM[s,i+1] cont'd. Q: If the transmitter is in state s at time i+1, what state(s) could it have been in at time i? A: For each state s, there are two predecessor states $\alpha$ and $\beta$ in the trellis diagram Example: for state 01, $\alpha$ =10 and $\beta$ =11. Any message sequence that leaves the transmitter in state s at time i+1 must have left the transmitter in state $\alpha$ or state $\beta$ at time i.

# Computing PM[s,i+1] cont'd.

Formalizing the computation:

 $PM[s,i+1] = min(PM[\alpha,i] + BM[\alpha \rightarrow s],$   $PM[\beta,i] + BM[\beta \rightarrow s])$ 

Example:

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PM[01,i+1] = min(PM[10,i] + 2, PM[11,i] + 1)= min(3+2,2+1) = 3

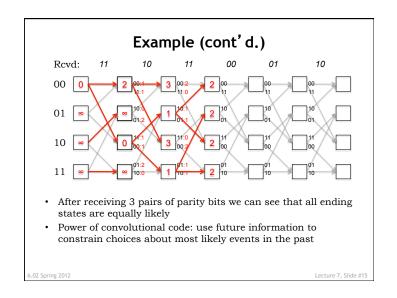
Notes:

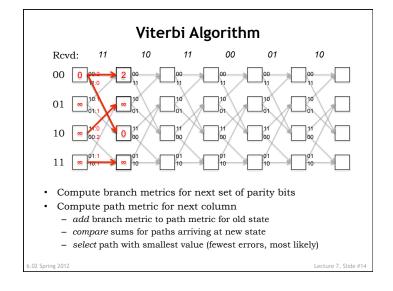
- 1) Remember which arc was min; saved arcs will form a path through trellis
- If both arcs have same sum, break tie arbitrarily (e.g., when computing PM[11,i+1])

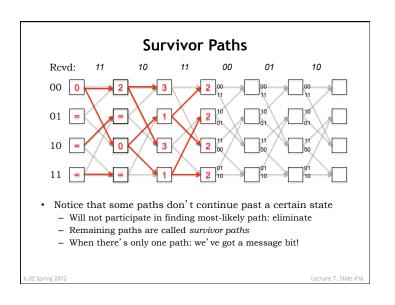
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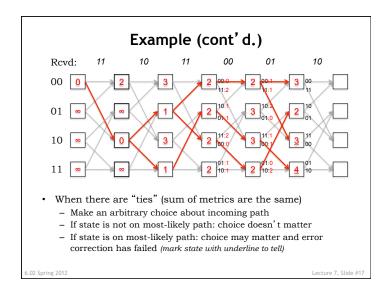
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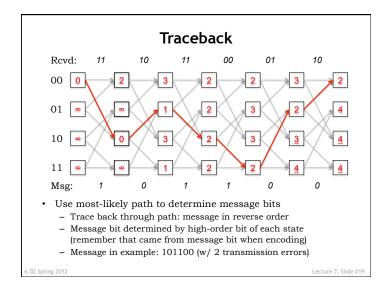


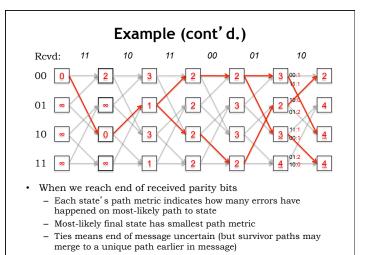




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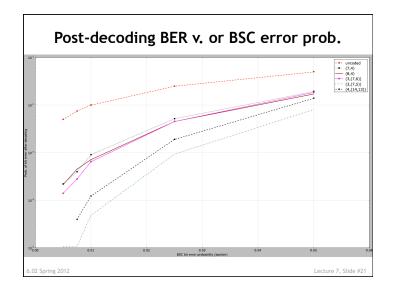


# Viterbi Algorithm with Hard Decisions

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- Branch metrics measure the likelihood by comparing received parity bits to possible transmitted parity bits computed from possible messages.
- Path metric PM[s,i] proportional to likelihood of transmitter being in state s at time i, assuming the mostly likely message of length i that leaves the transmitter in state s.
- Most likely message? The one that produces the most likely PM[s,N].
- At any given time there are 2<sup>K-1</sup> most-likely messages we're tracking → time complexity of algorithm grows exponentially with constraint length K.

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# **Soft-Decision Decoding**

- In practice, the receiver gets a voltage level, V, for each received parity bit
  - Sender sends V0 or V1 volts; V in (- $\infty$ , $\infty$ ) assuming additive Gaussian noise
- Idea: Pass received voltages to decoder **before** digitizing
- Define a "soft" branch metric as the square of the Euclidian distance between received voltages and expected voltages



 Soft-decision decoder chooses path that minimizes sum of the squares of the Euclidean distances between received and expected voltages

6.02 Spring 2842 Different BM & PM values, but otherwise the same algorithm 7, Slide #23

### **Hard Decisions**

- As we receive each bit it's immediately digitized to "0" or "1" by comparing it against a threshold voltage
  - We lose the information about how "good" the bit is:
     a "1" at .9999V is treated the same as a "1" at .5001V
- The branch metric used in the Viterbi decoder is the Hamming distance between the digitized received voltages and the expected parity bits
  - This is called hard-decision decoding
- Throwing away information is (almost) never a good idea when making decisions
  - Can we come up with a better branch metric that uses more information about the received voltages?

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# What determines the "goodness" of a convolutional code?

- How much error correcting capability do we get from a convolutional code?
- In general, larger values of K and r (the number of parity streams or generators) provide higher error tolerance
- But what determines the error correction ability? (I.e., what's the equivalent of the Hamming distance?)
- Answer: With hard-decision decoding, it is the free distance of the code

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