

INTRODUCTION TO EECS II DIGITAL COMMUNICATION SYSTEMS

Lecture 16, Slide #1

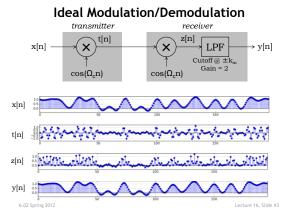
codeword

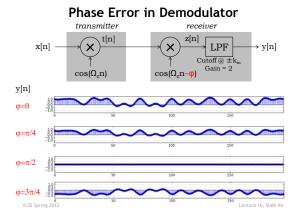
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bits in

6.02 Fall 2011 Lecture #16

- More on modulation and demodulation, FDM Effects of phase errors and channel delays .
- •
- Quadrature demodulation and more advanced modulation formats





From Baseband to Modulated Signal, and Back

modulate

NOISY & DISTORTING ANALOG CHANNEL

x[n]

demodulate

& filter

generate digitized

samples

t_D[n]

ADC

t[n]

sample &

threshold

DAC

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codeword

bits out

Lecture 16, Slide #2

Phase Error Math

Let's derive an equation for z[n]:

 $z[n] = t[n]\cos(\Omega_c n - \varphi) = x[n]\cos(\Omega_c n)\cos(\Omega_c n - \varphi)$

But

 $\cos(\Omega_c n)\cos(\Omega_c n - \varphi) = 0.5(\cos(2\Omega_c n - \varphi) + \cos(\varphi))$

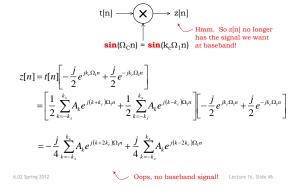
It follows that the demodulated output, after the LPF of gain 2 and cutoff frequency< $2\Omega_{\rm c},$ is

$y[n] = x[n]\cos(\varphi)$

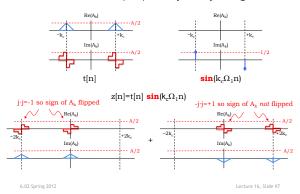
So a phase error of ϕ results in amplitude scaling by $\cos(\phi).$

Note: in the extreme case where $\varphi=\pi/2$, we are demodulating by a sine rather than a cosine, and we get y[n]=0. Lecture 16, Side #5

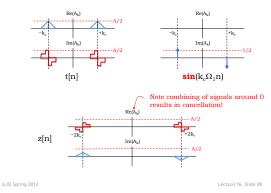
Demodulation with $sin(k_c \Omega_1 \mathbf{n})$

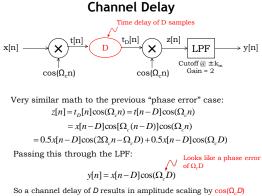


Demodulation (sin) Frequency Diagram



Demodulation (sin) Frequency Diagram





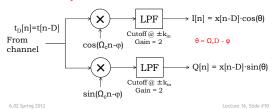
So a channel delay of *D* results in amplitude scaling by cos(Ω_c*D*) 6.02 Spring 2012 Lecture 16, Slide #9

Fixing Phase Problems in the Receiver

So phase errors and channel delay both result in a scaling of the output amplitude, where the magnitude of the scaling can't necessarily be determined at system design time: • Channel delay varies on mobile devices

· Phase difference between transmitter and receiver is arbitrary

One solution: quadrature demodulation



Quadrature Demodulation

If we let

y[n] = I[n] + jQ[n]

then

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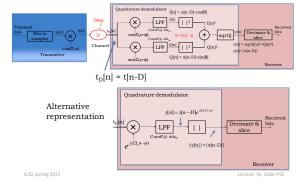
$$|y[n]| = \sqrt{I[n]^2 + Q[n]^2}$$

 $= |x[n-D]| \sqrt{\cos^2 \theta + \sin^2 \theta}$ = |x[n-D]|

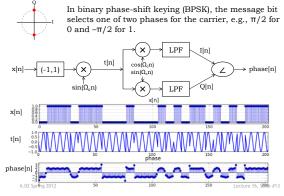
OK for recovering $\mathbf{x}[n]$ if it never goes negative, as in on-off keying

 $\frac{x[n-D]sin(\theta)}{x[n-D]cos(\theta)}$ Constellation diagrams (bit decimated x[n-D]): x[n-D] = {0, 1} it decimated x[n-D] = {0,

Full system view



BPSK

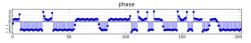


Dealing With Phase Ambiguity



BPSK is also subject phase changes introduced by channel delays or phase difference between xmit and rcv: the received constellation will be rotated with respect to the transmitter's constellation. Which phase corresponds to which bit?

The fix? Think of the phase encoding as *differential*, not absolute: a change in phase corresponds to a change in bit value. Assume that, by convention, messages start with a single 0 bit, i.e., prepend a 0 to each to message. Then the first phase change represents a $0 \rightarrow 1$ transition, the second phase change a $1 \rightarrow 0$ transition, and so on.

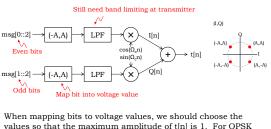


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QPSK Modulation

We can use the quadrature scheme at the transmitter too:



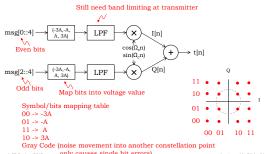
values so that the maximum amplitude of t[n] is 1. For QPSK (also referred to as QAM-4) that would mean $\left(\frac{1}{\sqrt{2}}, \frac{1}{\sqrt{2}}\right) = (.707, .707)$

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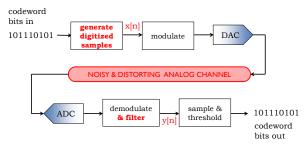
QAM Modulation

Use more levels in each arm (e.g. 4 levels per arm - 16QAM):



6.02 Spring 2012 only causes single bit errors) Lecture 16, Slide #16

From Baseband to Modulated Signal, and Back



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