Small Signal Switching Transistor

NPN Silicon

Features

- MIL-PRF-19500/255 Qualified
- Available as JAN, JANTX, and JANTXV

MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	50	Vdc
Collector - Base Voltage	V _{CBO}	75	Vdc
Emitter – Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	I _C	800	mAdc
Total Device Dissipation @ T _A = 25°C	P _T	500	mW
Total Device Dissipation @ T _C = 25°C	P _T	1.0	W
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +200	°C

THERMAL CHARACTERISTICS

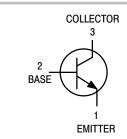
Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	325	°C/W	
Thermal Resistance, Junction to Case	$R_{\theta JC}$	150	°C/W	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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TO-18 CASE 206AA STYLE 1

ORDERING INFORMATION

Device	Package	Shipping
JAN2N2222A		
JANTX2N2222A	TO-18	Bulk
JANTXV2N2222A		

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	<u>, </u>		-	•
Collector – Emitter Breakdown Voltage (I _C = 10 mAdc)	V _(BR) CEO	50	-	Vdc
Collector-Base Cutoff Current (V _{CB} = 75 Vdc) (V _{CB} = 60 Vdc)	I _{CBO}	_ _	10 10	μAdc nAdc
Emitter-Base Cutoff Current (V _{EB} = 6.0 Vdc) (V _{EB} = 4.0 Vdc)	I _{EBO}	_ _	10 10	μAdc nAdc
Collector–Emitter Cutoff Current (V _{CE} = 50 Vdc)	I _{CES}	-	50	nAdc
ON CHARACTERISTICS (Note 1)	•	•	•	•
DC Current Gain $ \begin{array}{l} (I_{C}=0.1 \text{ mAdc, V}_{CE}=10 \text{ Vdc}) \\ (I_{C}=1.0 \text{ mAdc, V}_{CE}=10 \text{ Vdc}) \\ (I_{C}=10 \text{ mAdc, V}_{CE}=10 \text{ Vdc}) \\ (I_{C}=150 \text{ mAdc, V}_{CE}=10 \text{ Vdc}) \\ (I_{C}=500 \text{ mAdc, V}_{CE}=10 \text{ Vdc}) \end{array} $	h _{FE}	50 75 100 100 30	325 - 300 -	_
Collector – Emitter Saturation Voltage ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)	V _{CE(sat)}	_ _	0.3 1.0	Vdc
Base – Emitter Saturation Voltage ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)	V _{BE(sat)}	0.6 -	1.2 2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	
Magnitude of Small–Signal Current Gain ($I_C = 20$ mAdc, $V_{CE} = 20$ Vdc, $f = 100$ MHz)	h _{fe}	2.5	_	-
Small–Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1 kHz)	h _{fe}	50	_	-
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, 100 kHz \leq f \leq 1.0 MHz)	C _{ibo}	-	25	pF
Output Capacitance ($V_{CB} = 10 \text{ Vdc}, I_E = 0,100 \text{ kHz} \le f \le 1.0 \text{ MHz}$)	C _{obo}	_	8.0	pF
SWITCHING (SATURATED) CHARACTERISTICS	,	•	•	•
Turn-On Time (Reference Figure in MIL-PRF-19500/255)	t _{on}	_	35	ns
Turn-Off Time (Reference Figure in MIL-PRF-19500/255)	t _{off}	_	300	ns
				

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = $300 \mu s$, Duty Cycle $\leq 2.0\%$.

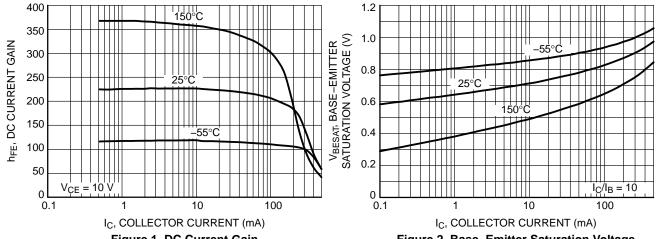


Figure 1. DC Current Gain



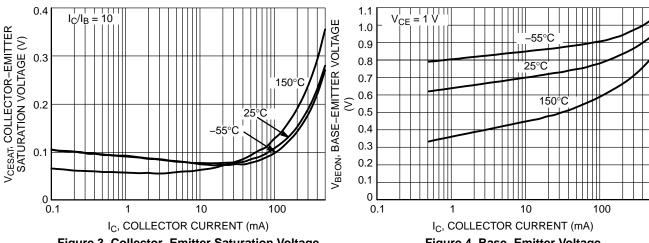


Figure 3. Collector-Emitter Saturation Voltage

Figure 4. Base-Emitter Voltage

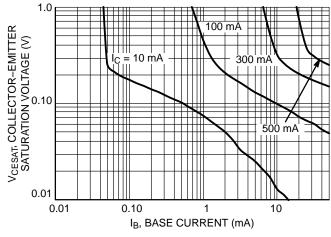


Figure 5. Collector Saturation Region

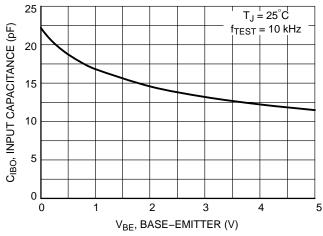


Figure 6. Input Capacitance

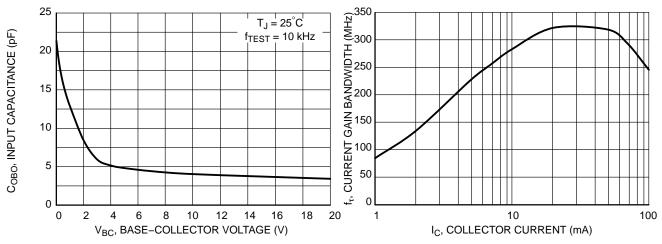
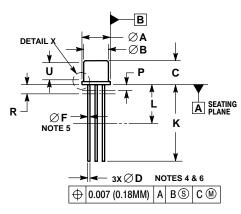


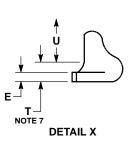
Figure 7. Output Capacitance

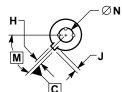
Figure 8. Current Gain Bandwidth Product

PACKAGE DIMENSIONS

TO-18 3 CASE 206AA ISSUE A









- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: INCHES.
 DIMENSION J MEASURED FROM DIAMETER A TO EDGE.
- LEAD TRUE POSITION TO BE DETERMINED AT THE GUAGE
- PLANE DEFINED BY DIMENSION R.
 DIMENSION F APPLIES BETWEEN DIMENSION P AND L.
- DIMENSION D APPLIES BETWEEN DIMENSION L AND K. BODY CONTOUR OPTIONAL WITHIN ZONE DEFINED BY DIMEN-
- SIONS A, B, AND T.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	5.31	5.84	0.209	0.230
В	4.52	4.95	0.178	0.195
С	4.32	5.33	0.170	0.210
D	0.41	0.53	0.016	0.021
E		0.76		0.030
F	0.41	0.48	0.016	0.019
Н	0.91	1.17	0.036	0.046
J	0.71	1.22	0.028	0.048
K	12.70	19.05	0.500	0.750
L	6.35		0.250	
M	45°BSC		45 °BSC	
N	2.54 BSC		0.100 BSC	
P		1.27		0.050
R	1.37 BSC		0.054 BSC	
T		0.76		0.030
U	2.54		0.100	

STYLE 1:

- **EMITTER** BASE
- COLLECTOR 3.

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