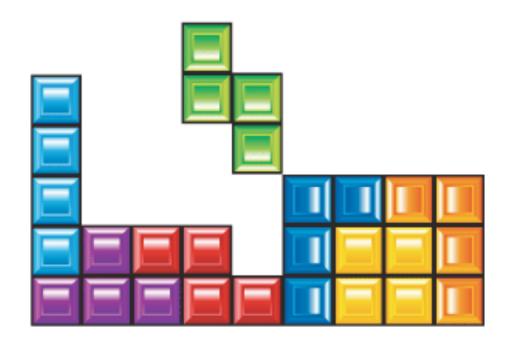
ImprovTetris

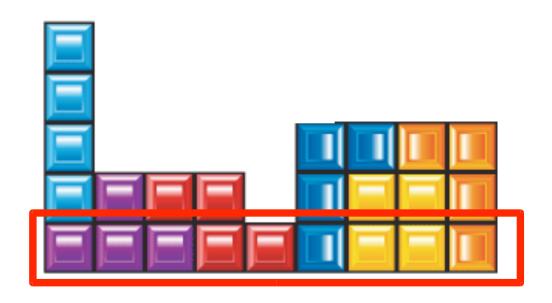
Scott Bezek Ray Li

11/15/11

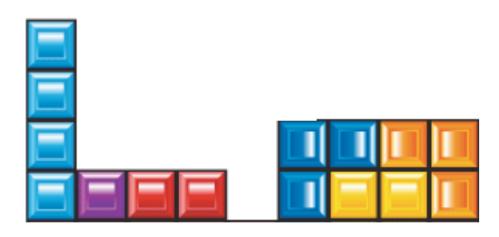
- Piece together blocks
- 7 possible blocks, picked randomly

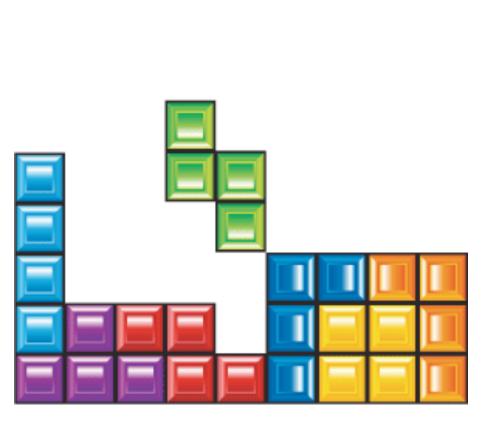


Clear complete rows



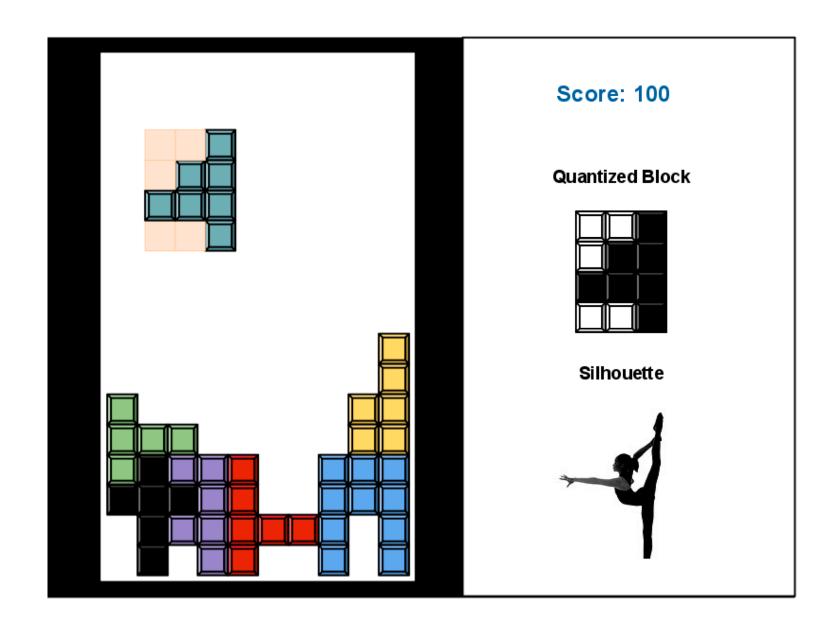
Clear complete rows



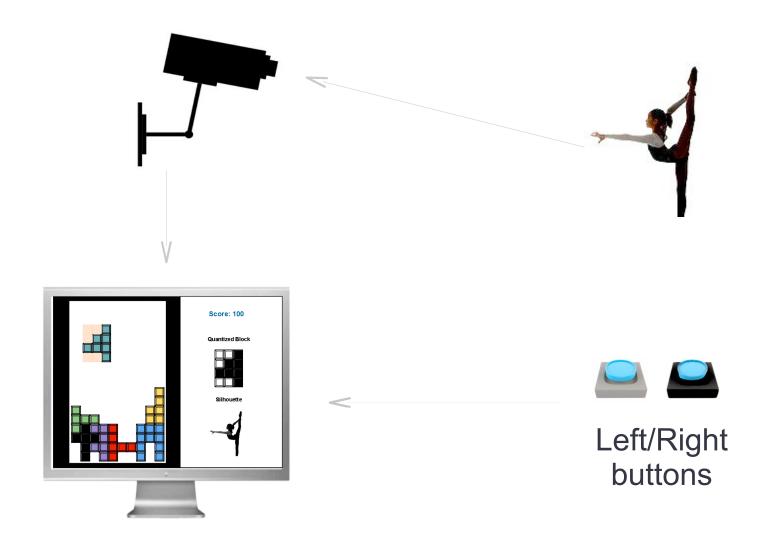




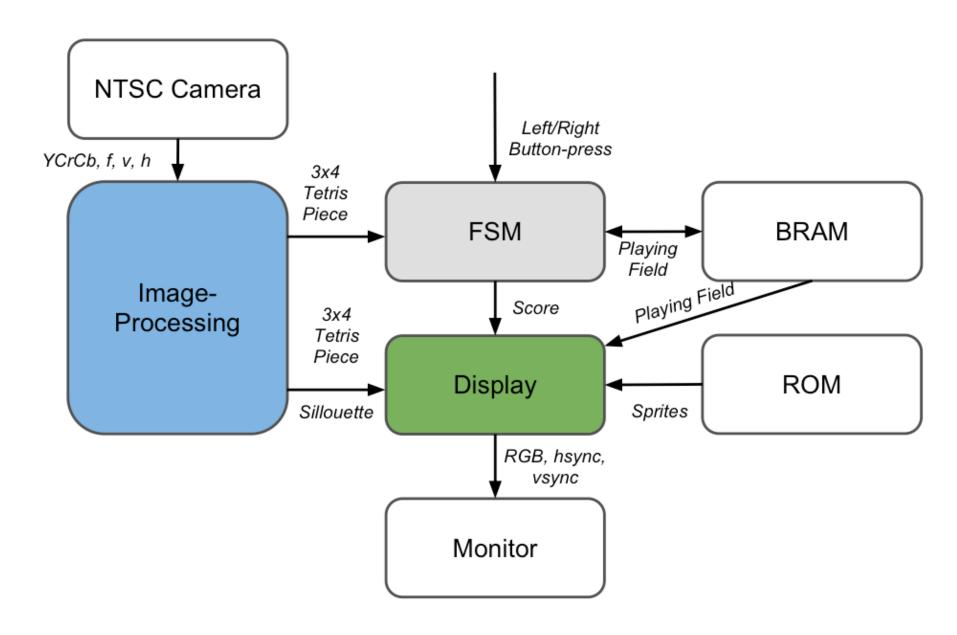
ImprovTetris



I/O



Block Diagram



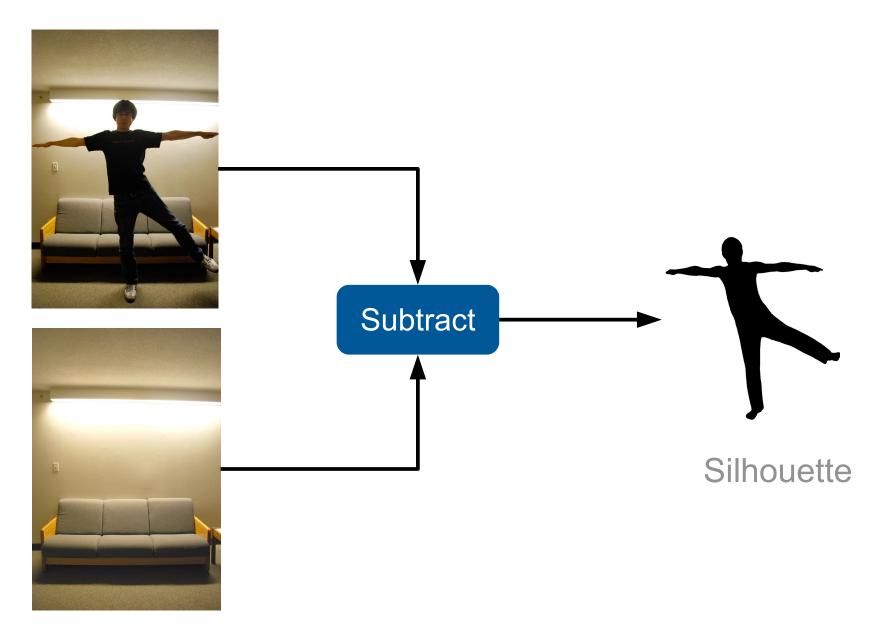
Current Frame



Reference Frame

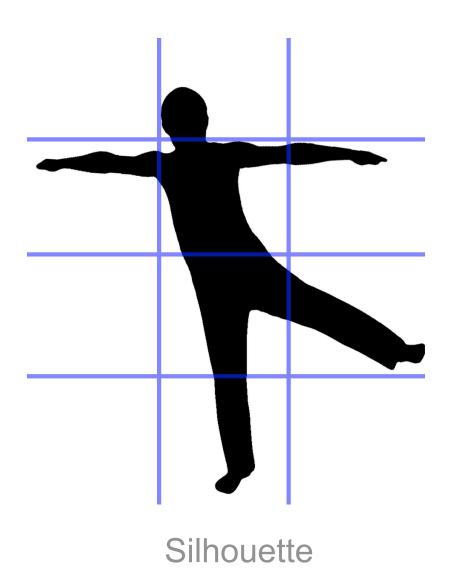


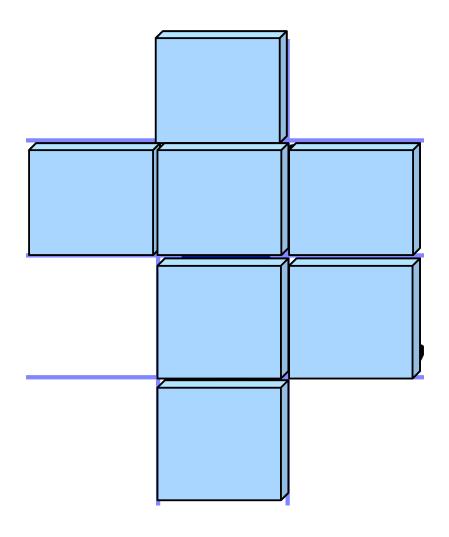
VS





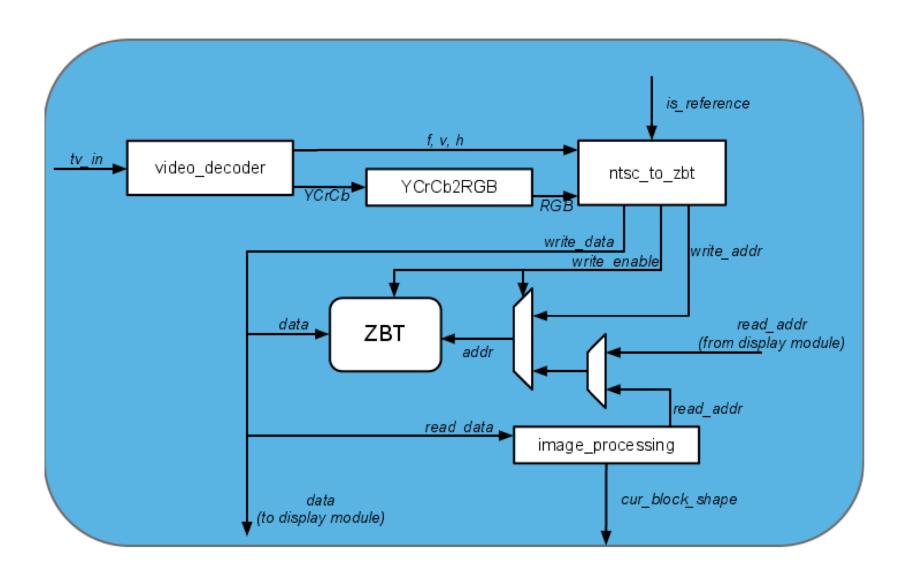
Silhouette

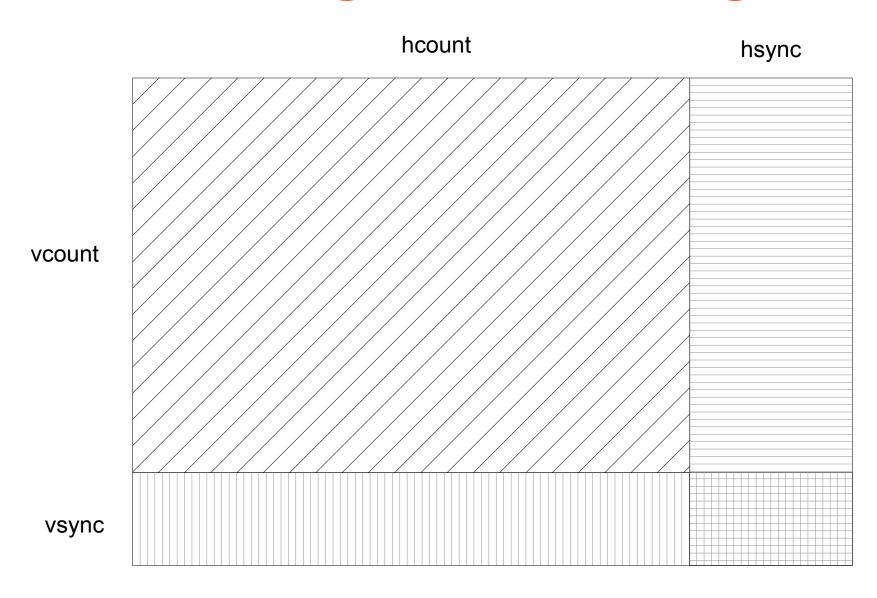




Quantized Block

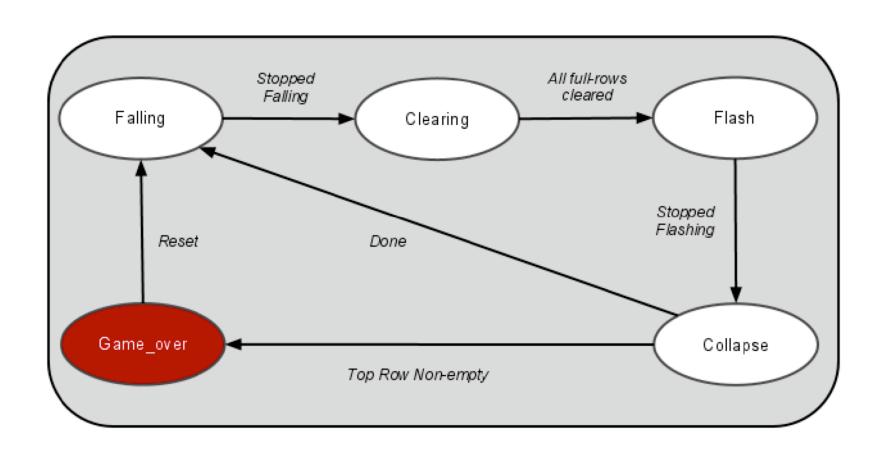
- Pixel-by-pixel subtraction (2 px per clock cycle)
- 4x4 pixel "blocks" increase SNR



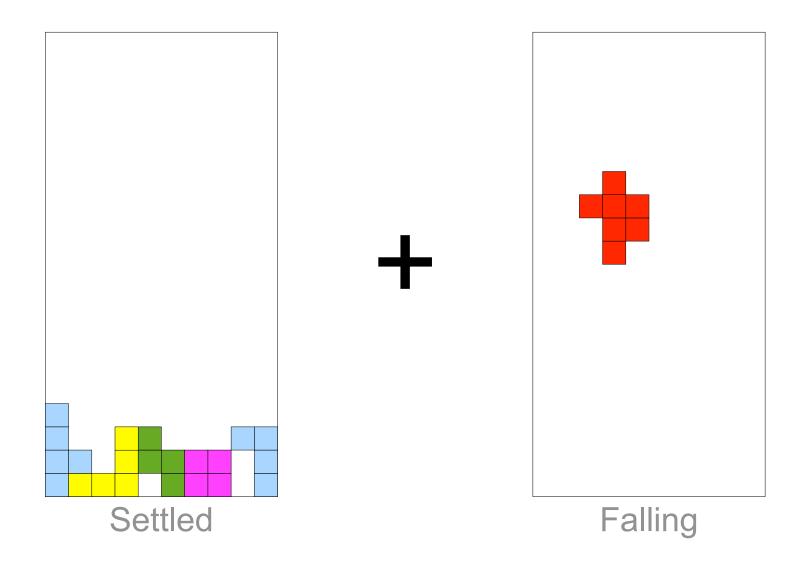


hcount hsync o image processing vcount Display video preview Save frame to ZBT memory vsync

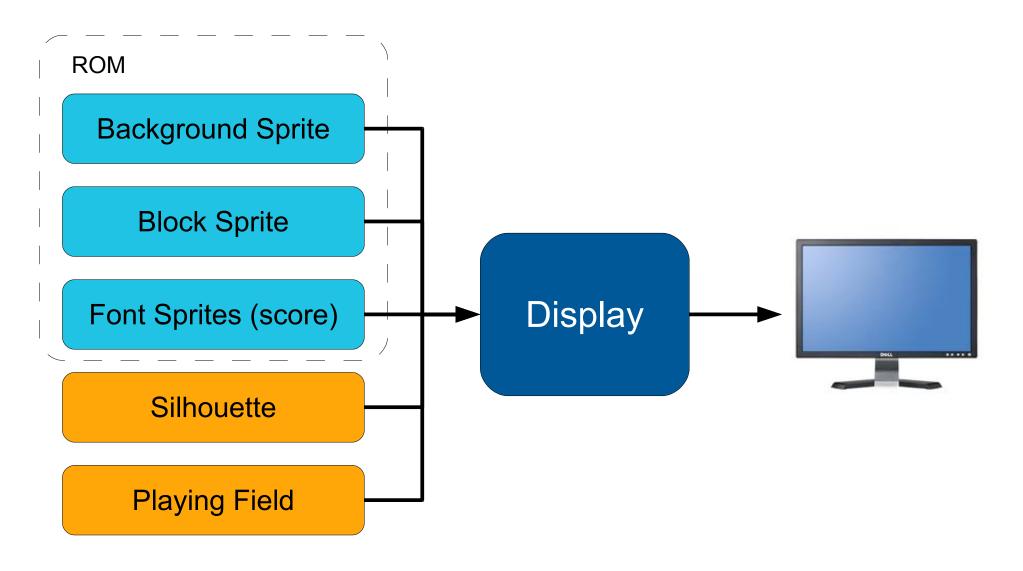
FSM (Game Logic)



Game State



Display Module



Major Challenges

- Timing constraints
 - Pixel clock speed ROM sprite lookup
 - Image processing takes many cycles
 - Memory address forecasting
- Noise from NTSC camera
- Shared memory access
 - ZBT camera write vs. processing read
 - BRAM FSM write vs. display read
 - Only one module can read/write at a time
 - Must arbitrate access

Timeline

Week	Ray's goals	Scott's goals
November 14	Implement FSM	Implement Silhouette Image Processing
November 21	Finish FSM. Test FSM and begin Display Module	Implement Block Quantization
November 28	Finish Display	Test and fine-tune Image-Processing Algorithms
December 5	Debug [Add audio effects] [Improve graphics]	Debug [Add Gyroscope] [Add two-player support]