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This document is designed to help you get started communicating with the Flash ROM. The interface shown here only allows you to write sequentially and read in any order: you write a stream of bytes to the ROM, and later you can read them back out as though it was a RAM.

This is all based on the tests written by Nathan Ickes, which you can find here: <http://www-mtl.mit.edu/Courses/6.111/labkit/verilog/004/flashtest/doc/flashtest.shtml> If you want to do better than the interface provided here, go back to these files.

There are three modules:

-flash_int.v

Does the very low level manipulation.

-test_fsm.v

Does the medium level manipulation (heavily modified from the test code linked above)

-flash_manager.v

Does the high level manipulation. This is the ONLY thing which should be in your top-level labkit, it instantiates a flash_int and test_fsm for itself.