Autonomous RC Racecar

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Overview of Project

- Build an autonomous RC race car to drive on *any* track.
- Draw a map on a whiteboard and have the car drive the track automatically.
Motivation

- Interest in using advanced peripherals for the FPGA
- Sought a project that was suited to FPGA’s purposes
- Popularity of driverless cars, Stanford’s Shelley Autonomous Car
- ... because racecar!
Physical Set up

- Used car with IR led, and Camera with IR filter to detect car position (Camera 1)
- Camera 2 to process the track
Paper Track Processing

- YUV 4:2:2 Format is used
- Converts picture into grayscale using Y information
- Classify each pixel as a part of track or not
- Identify regions of the track: Outside the track, track, inside track
Example Track (Track is closed loop)

- Line scan algorithm will be used to classify pixels as boundary, outside the track, and inside the track.
- Assumes that track is fully closed inside the whiteboard area. (No track edge can be in whiteboard area.)
Car Position Processing

- Car controller requires visual feedback
- LED’s on roof of car to identify position and heading
- Use camera with IR band-pass filter to make center of mass determination easier
- Special considerations:
  - Speed
    - High video FPS, fast algorithms to locate car
    - Asynchronous with video FPS
  - Accuracy
Car Position Processing

Center of mass calculation for each blob:

- Thresholding
- Calculation of mean x and y coordinates for “blobs” of adjacent white pixels
Car Controller

Two primary purposes:

- Determine corrective actions to stay on track
- Send proper commands to RC car
Car Controller will receive the region of the map it is currently in and will be if it’s movement continues.

Current scheme doesn’t require current position but potentially could if eventually required.

<table>
<thead>
<tr>
<th>Current Car Region</th>
<th>Predicted Car Region</th>
<th>Desired Command</th>
</tr>
</thead>
<tbody>
<tr>
<td>Outer Track</td>
<td>Outer Track</td>
<td>Right</td>
</tr>
<tr>
<td>Outer Track</td>
<td>Track</td>
<td>Forward</td>
</tr>
<tr>
<td>Outer Track</td>
<td>Inner Track</td>
<td>Left</td>
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<tr>
<td>Track</td>
<td>Outer Track</td>
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</table>
Car Controller - Sending RC commands

Cheap and lazy transmission scheme, different number of W1 27MHz pulses

10 pulses = Forward
28 pulses = Forward + Left
64 pulses = Right
Car Controller - Sending RC commands

Current implementation, FET’s are used in parallel with the normal buttons.

Fast to implement but lacks precision

May be able to hijack RF circuitry to gain more precision
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</thead>
<tbody>
<tr>
<td><strong>All</strong></td>
<td><em>Rough Proposal Draft</em></td>
<td>Project Design presentation</td>
<td>Revised Proposal, Project Checklist</td>
<td>Thanksgiving week</td>
<td>Buffer week</td>
<td>Done</td>
</tr>
<tr>
<td>Battushig</td>
<td>Recognize track, generate track in memory</td>
<td>Recognize track, generate track in memory</td>
<td>Integration</td>
<td>Testing</td>
<td></td>
<td></td>
</tr>
<tr>
<td>David</td>
<td>Get car under control from FPGA</td>
<td>Given heading and position, control loops</td>
<td>Integration</td>
<td>Testing</td>
<td></td>
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</tr>
<tr>
<td>Kevin</td>
<td>Identify car position, heading. Basic mapping of camera space to map space</td>
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<td>Testing</td>
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Stretch Goals

- Gamify
  - Player vs. computer
  - Checkpoints
- Lap timing
- Optimize car control for speed
Summary of Key Challenges

● Paper Track Processing
  ○ Detection of track boundaries and assigning regions to track map in memory

● Car Position Processing
  ○ Finding center of masses of white “blobs” as seen by VGA camera with IR bandpass filter

● Car Control
  ○ Smooth and fast control of car
  ○ Working around serial communication protocol of RC car
Let’s go racing!

Questions?