FPGA Telephone Checkoff

**Commitment:**
- Finite State Machine
  - Make calls
  - Receive calls
- Communication
  - Two way audio without noise
  - (2) One directional wire (and GND)

**Goal:**
- Finite State Machine
  - Store and recall voice messages
- Communication
  - (1) Bi-directional wire

**Stretch:**
- Store and recall multiple messages
- Connect more than two FPGAs
- Graphics