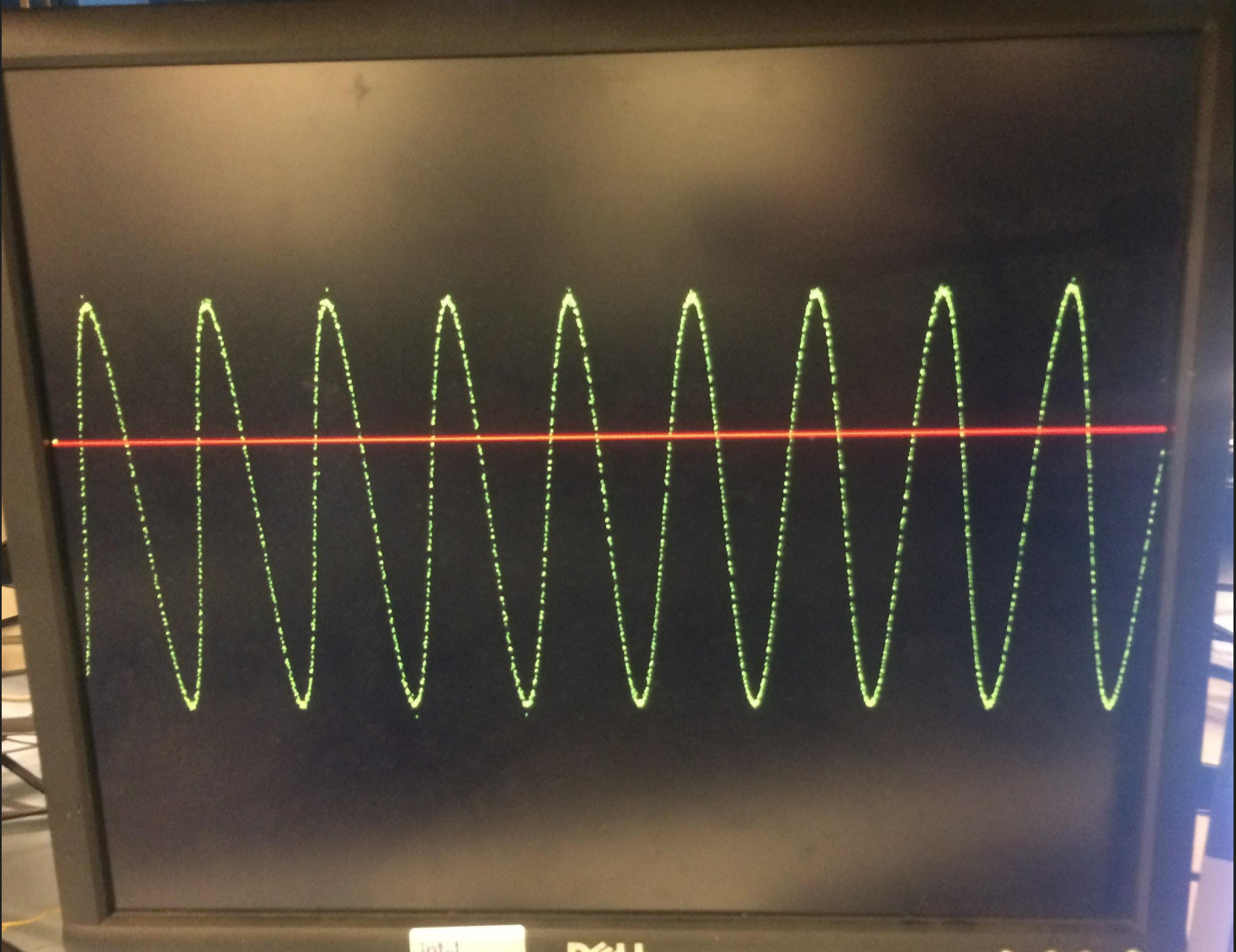


# Digital Storage Oscilloscope

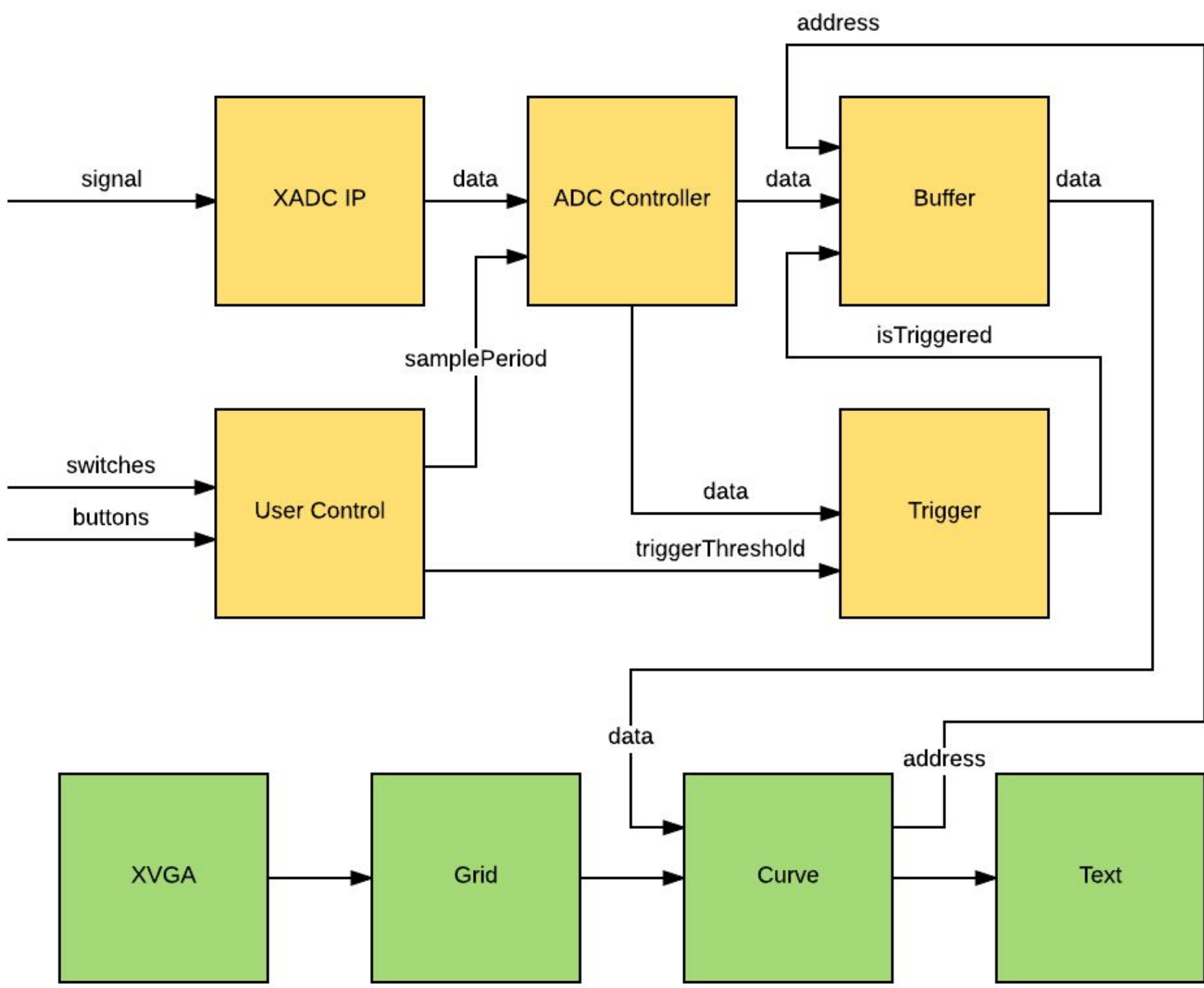
Daniel Richman & Jorge Troncoso

10 November 2016

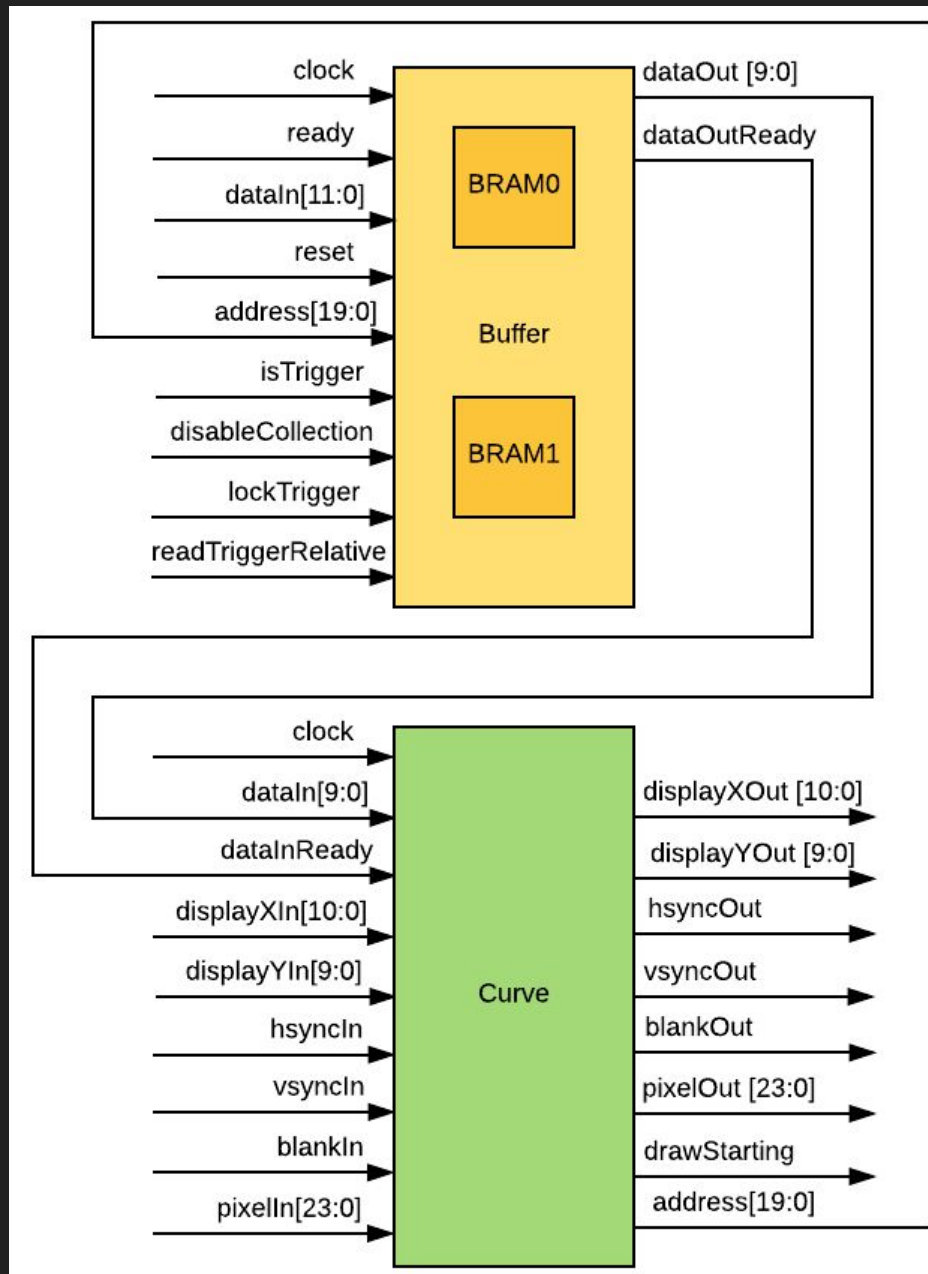
6.111



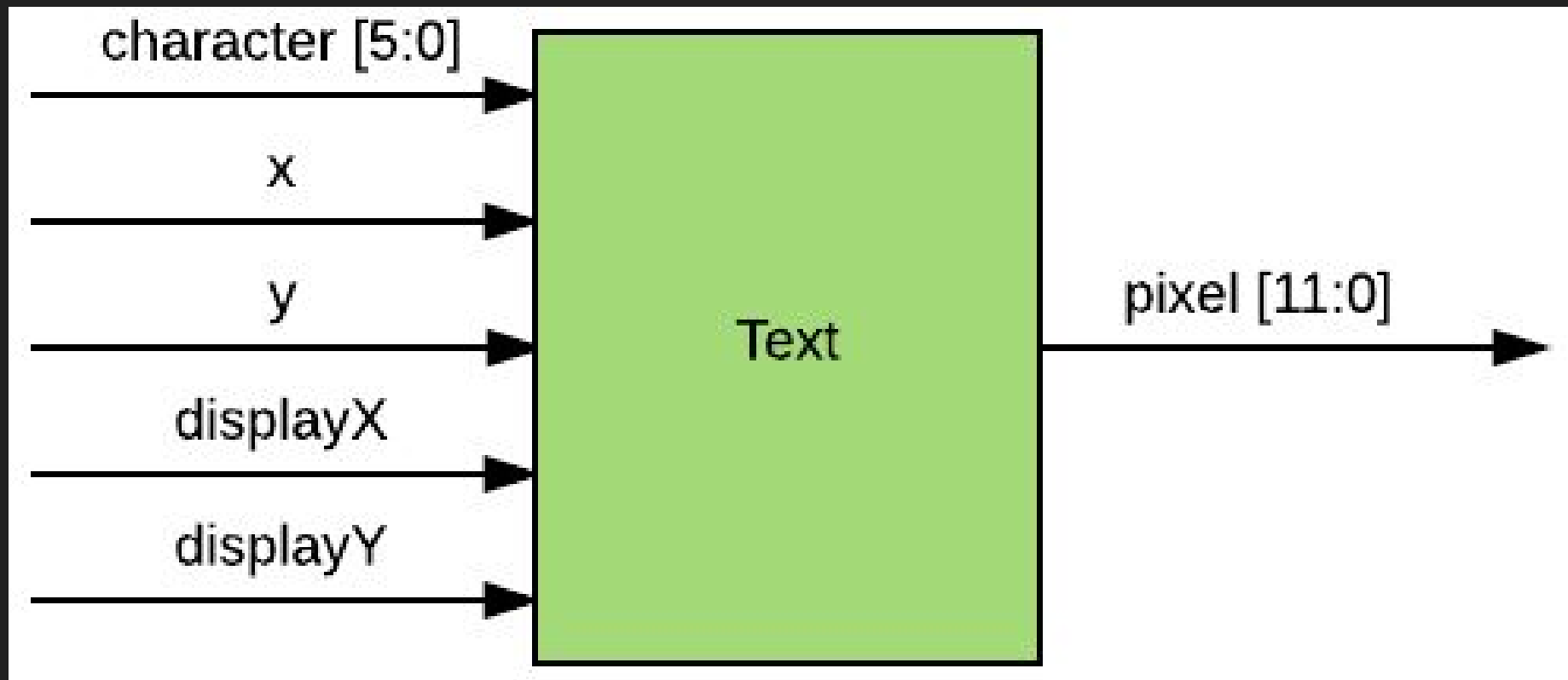
- Customizable triggering system
- Scalable X-Y axes
- Autoset
- Cursors and measurements
- Run/stop
- Signal analysis: FFT



# Major Modules: Curve & Buffer

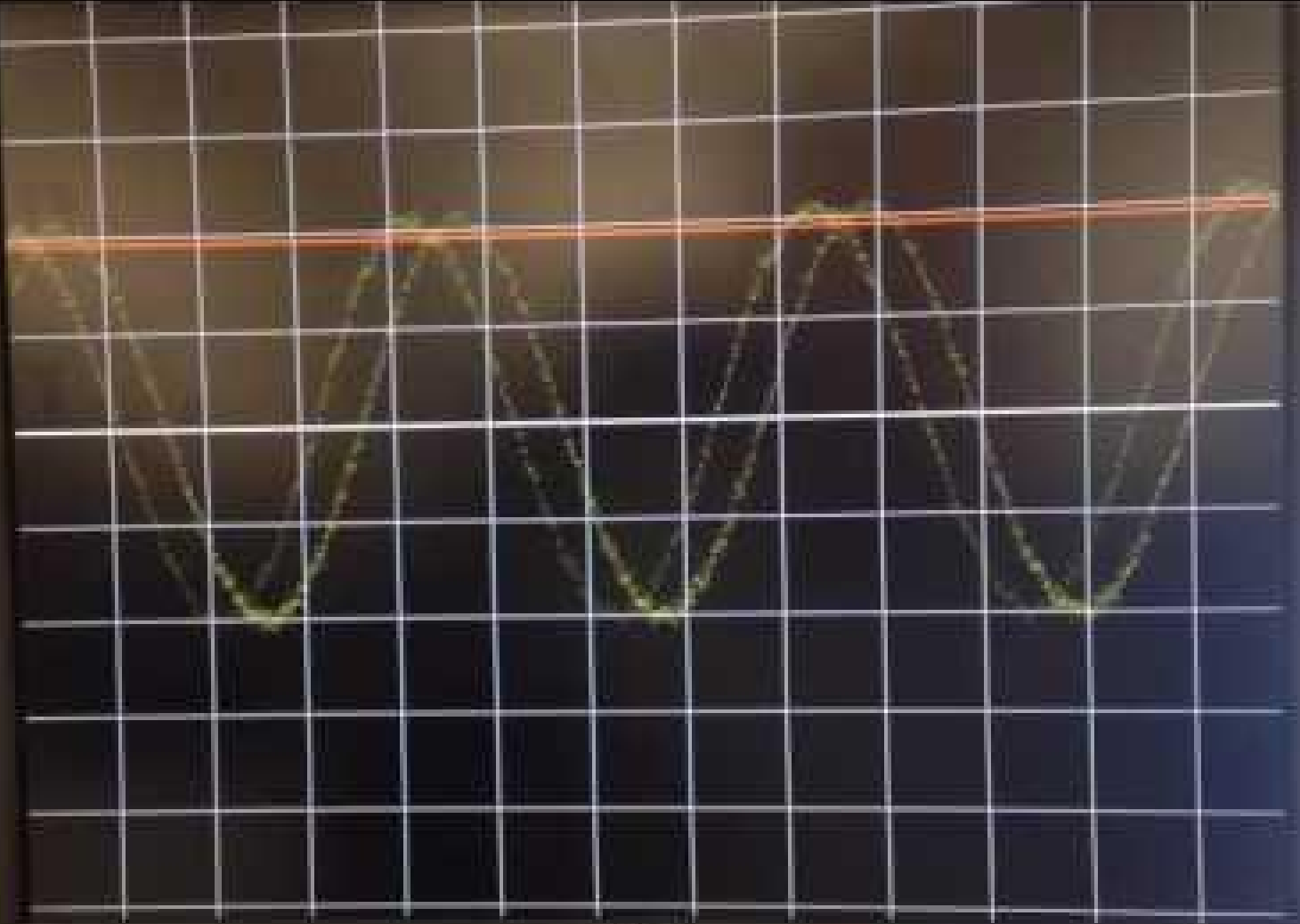


## Major Modules: Text



# Autoset

- Automatically determines appropriate
  - Trigger level
  - Vertical scaling
  - Horizontal scaling
  - Vertical position
- Challenges
  - Autoset must read data from the buffer.
  - Tricky because BRAMs only have 2 read/write ports.



DATE

# Timeline: Jorge

<b>Date</b>	<b>Goal</b>
November 11th	- Grid implemented
November 18th	- Text module implemented
November 25th (Thanksgiving)	
December 2nd	- Cursors & Measurements implemented - Run/Stop implemented
December 9th	- Multiple Channels



# Timeline: DDR

<b>Date</b>	<b>Goal</b>
November 11th	- Autoset implemented
November 18th	- Analog biasing/scaling circuit tested
November 25th (Thanksgiving)	
December 2nd	- Frame averaging - X/Y display mode
December 9th	- FFT