

Logic Analyzer
Probe Names

analyzer3_data[15:0]

CK0

A2	0	0	_____
	1	1	_____
	2	2	_____
	3	3	_____
	4	4	_____
	5	5	_____
	6	6	_____
	7	7	_____

A3	0	8	_____
	1	9	_____
	2	10	_____
	3	11	_____
	4	12	_____
	5	13	_____
	6	14	_____
	7	15	_____

Logic Analyzer
Probe Names

analyzer1_data[15:0]

CK1

A0	0	0	_____
	1	1	_____
	2	2	_____
	3	3	_____
	4	4	_____
	5	5	_____
	6	6	_____
	7	7	_____

A1	0	8	_____
	1	9	_____
	2	10	_____
	3	11	_____
	4	12	_____
	5	13	_____
	6	14	_____
	7	15	_____