

Bonnie Lam

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EDUCATION

MASSACHUSETTS INSTITUTE OF TECHNOLOGY

PHD IN EECS

Expected Jun 2016 | Cambridge, MA

ASIC for Ultrasound Beamforming

Minor in Finance

Cum. GPA: 5.0 / 5.0

SM IN EECS

Grad. Jun 2010 | Cambridge, MA

Low-Power Video Entropy Coding

Cum. GPA: 5.0 / 5.0

UNIVERSITY OF BRITISH COLUMBIA

BASC IN ENGINEERING PHYSICS

Grad. May 2008 | Vancouver, BC

Cum. GPA: 4.0 / 4.0

LINKS

LinkedIn:// [bonniekylam](#)

Twitter:// [@bkylaMIT](#)

COURSEWORK

GRADUATE

Computer Systems Architecture

Digital Image Processing

Discrete-Time Signal Processing

Power Electronics

Digital Circuit Design

UNDERGRADUATE

Discrete Structures & Algorithms

Progr. Design & Data Structures

Economic Analysis of Eng. Projects

Technical Communications

Circuit Analysis I & II

SKILLS

PROGRAMMING

Matlab • Python • Perl • C

C++ • Shell • C# • \LaTeX

LANGUAGES

Mother Tongue

English • Cantonese Chinese

Fluent

Mandarin Chinese

Basic

French

EXPERIENCE

STARTMIT ENTREPRENEURIAL WORKSHOP (FORMERLY START6)

Jan – Mar 2015 | Massachusetts Institute of Technology | Cambridge, MA

- Developed product with focus on user, marketing, and value proposition.
- Presented product ideation to stakeholders with varying levels of expertise.

TEXAS INSTRUMENTS INC. | PHD INTERN

June – Aug 2012, 2011 | Medical Business Unit | Dallas, TX

- Conducted feasibility study and defined metrics for a hardware beamformer.
- Analyzed and identified system components that dominate power and cost.

June – Aug 2010 | Kilby Labs Research | Dallas, TX

- Developed vision and roadmap for a distributed sensor/actuator system to monitor and reduce building energy usage.
- Pitched product to potential customers and integrated user feedback.

BROADCOM CANADA LTD. | HARDWARE SYSTEMS ENG. INTERN

May – Aug 2007 | Richmond, BC

- Prototyped reverse-polarity-protected PCB to enhance user experience.

INTEL OF CANADA, LTD. | SYSTEM VALIDATION & DESIGN INTERN

May – Dec 2006 | Vancouver, BC

- Collaborated with cross-site team to design efficient UI for IC validation.

LEADERSHIP

GWAMIT LEADERSHIP CONFERENCE | CONFERENCE CHAIR

Jul 2013 – Oct 2013 | Cambridge, MA

MTL ANNUAL RESEARCH CONFERENCE | COMMITTEE MEMBER

Sep 2013 – Jan 2014 | Bretton Woods, NH

TEACHING

"PREP FOR UNDERGRAD RESEARCH" | TEACHING ASSISTANT

Feb 2016 – May 2016 | Cambridge, MA

"CIRCUITS & ELECTRONICS 1/2/3" | COURSE INSTRUCTOR

Sep 2014 – Present | Cambridge, MA

- Architected MIT course into three self-paced modules to increase enrollment.

"DIGITAL INTEGRATED CIRCUITS" | TEACHING ASSISTANT

Sep 2012 – Dec 2012 | Cambridge, MA

- Consulted on student projects to find various use cases for design techniques.

AWARDS (SELECTED)

2016	Second Place Award, MIT Can Talk: Speaking Competition
2014, 2013, 2010	Best Presentation Award, MTL Annual Research Conference
2013	Featured Talk Award, MTL Annual Research Conference
2012	Texas Instruments Fellowship for Women in Microelectronics
2008	MIT Energy Fellow, MIT Energy Initiative, sponsored by Eni