Intel provides fellowships to outstanding PhD students in Engineering, Computer Science, Social Science, Human-Centered & Interaction Design, and other related majors focusing on semiconductor and computing technologies at selected U.S. universities.

**Award Provisions**
The fellowship includes a cash award (for one academic year) and a Research Total Industry Experience (TIE) grant. Award recipients are each assigned to an Intel technical mentor who is a respected leader in their field. Students work directly with their mentor to develop a deeper understanding of relevant technical issues and contribute to solving the most complex technical problems facing the industry. Award recipients are also prioritized for internships and hiring opportunities within the company.

Through the Research TIE grant, the Intel PhD fellows partner closely with their respective mentors in designing a personalized experience to meet and network with top industry researchers at a selected Intel campus during the academic year immediately following the award. The goal of this experience is to provide each student with an opportunity to showcase their research, identify potential industry-academic collaborations, and explore internship opportunities.

**Eligibility Criteria**
- Must be currently enrolled in an accredited PhD program at one of the participating universities with research concentration in one of the following technology areas:
  - Applications, Programming and New Usage Models
  - Computing Leadership
  - Semiconductor Innovation
    - Beyond CMOS devices and architectures; Bio inspired new device and architecture for future computation and memory; Bottoms up, self-aware fabrication for package interconnects and substrates; Compliant/Flexible non-fatiguing interconnects; and Novel devices/architectures for power neutral systems.
- Must have completed 24 months in the PhD program.
- Must be a U.S. citizen, legal permanent resident or H1-B visa holder.
  - This is subject to current U.S. Department of Commerce restrictions on Export Licensing and not open to individuals from embargoed or controlled countries.
- Must retain full-time student status during academic year for which fellowship is awarded.
- Intel employees and their families are not eligible.
Selection

- If selected, student may not defer the fellowship.
- Final awards are competitive and based on the student’s record of academic excellence, letters of recommendation and ability to clearly articulate the value of their research.
- Payment of the fellowship award is made directly to the university and is non-transferable.

Application

Select U.S. universities are invited annually to submit a limited number of student candidates for consideration. Students must first be nominated by their corresponding university and then are judiciously reviewed by Intel fellows and technical leaders to select the most qualified award recipients. Intel values diversity and encourages applicants from diverse backgrounds, under-represented minority groups, and women to apply.

Intel does not accept direct applications from students or faculty. Applications must be submitted via the online website. Once applicants are nominated by their respective Intel designated university coordinator, they are given a link, username, and unique password to access the application form. Please contact your university’s graduate school to determine if your Institution is currently participating in the program.

Timeline

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<td>January 08, 2014:</td>
<td>Program Launch</td>
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<td>February 28, 2014:</td>
<td>Nominations from Universities due to Intel</td>
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<td>March 03 - April 04, 2014:</td>
<td>Applications Accepted</td>
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Disclaimers and Notice

- Implementation and management of this Program and associated awards is subject to change at any time without notice to nominators or nominees or awardees and is at the complete discretion of Intel Corporation.
- Awardees are responsible for confirming that acceptance of any awards will not be in violation of any university policy regarding such awards and that acceptance of any award is not in exchange for promotion or influence regarding any of Intel’s commercial activities, products, services, or the adoption Intel-related standards.
- Any information collected as part of the PhD Fellowship Program will be used for the administration of the program. This may include sharing of any submitted information within Intel for purposes of selecting awardees, planning visits, public relations (with prior approval), or other purposes reasonably related to the Program. Any information collected is also subject to Intel’s privacy policy found at www.intel.com/privacy.

For additional program information please visit our website at www.intelfellowships.com.
Recommendations for Applicants

• Use the space wisely
• Introduce the work by concisely placing the work in context. For example, “My research is in the broad area of transistor physics.”
• Make clear what problem you are trying to solve. For example, “The problem I am trying to solve is improving the external resistance of double gate transistor devices”
• Quickly move into explaining YOUR project. For example, “My research focuses around developing new alloys for improved barrier height at the contact silicide boundary. I am primary focused on Ni/Ti/C alloys of ... “
• Helpful Tips:
  1. Consider your audience to be a professor that has just joined your department who is knowledgeable in your area but not necessarily the world’s expert.
  2. Don’t spend a lot of time/space on very high level explanations (for example, it is not necessary to emphasize the importance of low power, high performance, high yield etc.)
  3. Emphasize your research work, not the work of others in your group.
  4. Include enough details to validate your research approach and results, but no more (aka Einstein’s “as simple as possible, but no simpler”).