**General Description**

The LM158 series consists of two independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. Application areas include transducer amplifiers, dc gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM158 series can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional ±15V power supplies.

The LM358 is also available in a chip sized package (8-Bump micro SMD) using National’s micro SMD package technology.

**Unique Characteristics**

- In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage.
- The unity gain cross frequency is temperature compensated.
- The input bias current is also temperature compensated.

**Advantages**

- Two internally compensated op amps
- Eliminates need for dual supplies
- Allows direct sensing near GND and \( V_{OUT} \) also goes to GND
- Compatible with all forms of logic
- Power drain suitable for battery operation
- Pin-out same as LM1558/LM1458 dual op amp

**Features**

- Available in 8-Bump micro SMD chip sized package, (See AN-1112)
- Internally frequency compensated for unity gain
- Large dc voltage gain: 100 dB
- Wide bandwidth (unity gain): 1 MHz (temperature compensated)
- Wide power supply range:
  - Single supply: 3V to 32V
  - or dual supplies: ±1.5V to ±16V
- Very low supply current drain (500 µA)—essentially independent of supply voltage
- Low input offset voltage: 2 mV
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing: 0V to \( V^- - 1.5V \)

**Voltage Controlled Oscillator (VCO)**

![Voltage Controlled Oscillator Diagram]

DS007787-23

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Absolute Maximum Ratings (Note 9)
If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

<table>
<thead>
<tr>
<th></th>
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</thead>
<tbody>
<tr>
<td>Supply Voltage, V+</td>
<td>32V</td>
<td>26V</td>
<td></td>
</tr>
<tr>
<td>Differential Input Voltage</td>
<td>32V</td>
<td>26V</td>
<td></td>
</tr>
<tr>
<td>Input Voltage</td>
<td>−0.3V to +32V</td>
<td>−0.3V to +26V</td>
<td></td>
</tr>
<tr>
<td>Power Dissipation (Note 1)</td>
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<tr>
<td>Molded DIP</td>
<td>830 mW</td>
<td>830 mW</td>
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</tr>
<tr>
<td>Metal Can</td>
<td>550 mW</td>
<td>550 mW</td>
<td></td>
</tr>
<tr>
<td>Small Outline Package (M)</td>
<td>530 mW</td>
<td>530 mW</td>
<td></td>
</tr>
<tr>
<td>micro SMD</td>
<td>435 mW</td>
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<tr>
<td>Output Short-Circuit to GND</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>(One Amplifier) (Note 2)</td>
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<tr>
<td>V+ ≤ 15V and TA = 25°C</td>
<td>Continuous</td>
<td>Continuous</td>
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<tr>
<td>Input Current (VIN &lt; −0.3V) (Note 3)</td>
<td>50 mA</td>
<td>50 mA</td>
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<td>Operating Temperature Range</td>
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<tr>
<td>LM358</td>
<td>0˚C to +70˚C</td>
<td>−40˚C to +85˚C</td>
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<tr>
<td>LM258</td>
<td>−25˚C to +85˚C</td>
<td>−25˚C to +85˚C</td>
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<tr>
<td>LM158</td>
<td>−55˚C to +125˚C</td>
<td>−55˚C to +125˚C</td>
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<tr>
<td>Storage Temperature Range</td>
<td>−65˚C to +150˚C</td>
<td>−65˚C to +150˚C</td>
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<td>Lead Temperature, DIP</td>
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<tr>
<td>(Soldering, 10 seconds)</td>
<td>260˚C</td>
<td>260˚C</td>
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<td>Lead Temperature, Metal Can</td>
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<td>(Soldering, 10 seconds)</td>
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<td>Soldering Information</td>
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<tr>
<td>Dual-In-Line Package</td>
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<td>Soldering (10 seconds)</td>
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<td>260˚C</td>
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<tr>
<td>Small Outline Package</td>
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<tr>
<td>Vapor Phase (60 seconds)</td>
<td>215˚C</td>
<td>215˚C</td>
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<tr>
<td>Infrared (15 seconds)</td>
<td>220˚C</td>
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<tr>
<td>See AN-450 “Surface Mounting Methods and Their Effect on Product Reliability” for other methods of soldering surface mount devices.</td>
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<tr>
<td>ESD Tolerance (Note 10)</td>
<td>250V</td>
<td>250V</td>
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Electrical Characteristics
V+ = +5.0V, unless otherwise stated

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<thead>
<tr>
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<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>(Note 5), TA = 25˚C</td>
<td>1</td>
<td>2</td>
<td>2</td>
<td>3</td>
<td>2</td>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td>mV</td>
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<tr>
<td>Input Bias Current</td>
<td>IIN(+) or IIN(−), VA = 25˚C, VCM = 0V, (Note 6)</td>
<td>20</td>
<td>50</td>
<td>45</td>
<td>100</td>
<td>45</td>
<td>150</td>
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<td>nA</td>
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<tr>
<td>Input Offset Current</td>
<td>IIN(+) − IIN(−), VCM = 0V, TA = 25˚C</td>
<td>2</td>
<td>10</td>
<td>5</td>
<td>30</td>
<td>3</td>
<td>30</td>
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<td>nA</td>
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<tr>
<td>Input Common-Mode Voltage Range</td>
<td>V+ = 30V, (Note 7), (LM2904, V+ = 26V), TA = 25˚C</td>
<td>0</td>
<td>V+−1.5</td>
<td>0</td>
<td>V+−1.5</td>
<td>0</td>
<td>V+−1.5</td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Supply Current</td>
<td>Over Full Temperature Range, RL = ∞ on All Op Amps</td>
<td>1</td>
<td>2</td>
<td>1</td>
<td>2</td>
<td>1</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>V+ = 30V (LM2904 V+ = 26V)</td>
<td>0.5</td>
<td>1.2</td>
<td>0.5</td>
<td>1.2</td>
<td>0.5</td>
<td>1.2</td>
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<td>mA</td>
</tr>
<tr>
<td></td>
<td>V+ = 5V</td>
<td></td>
<td></td>
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</tbody>
</table>
### Electrical Characteristics

\(V^+ = +5.0\text{V},\) unless otherwise stated

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM358</th>
<th>LM2904</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>(V + = 30\text{V}, T_A = 25^\circ\text{C})</td>
<td>0</td>
<td>(V^+ - 1.5)</td>
<td>V</td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>(V + = 5\text{V})</td>
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</table>

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
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<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>(T_A = 25^\circ\text{C})</td>
<td>2</td>
<td>7</td>
<td></td>
<td>2</td>
<td>7</td>
<td>mV</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>(I_{\text{IN}(+) or I_{\text{IN}(-)}, V_{\text{CM}} = 0\text{V}, T_A = 25^\circ\text{C}})</td>
<td>45</td>
<td>250</td>
<td></td>
<td>45</td>
<td>250</td>
<td>nA</td>
</tr>
<tr>
<td>Input Offset Current</td>
<td>(I_{\text{IN}(+) - I_{\text{IN}(-)}, V_{\text{CM}} = 0\text{V}, T_A = 25^\circ\text{C}})</td>
<td>5</td>
<td>50</td>
<td></td>
<td>5</td>
<td>50</td>
<td>nA</td>
</tr>
<tr>
<td>Input Common-Mode Voltage Range</td>
<td>(V^+ = 30\text{V}, (Note 7))</td>
<td>0</td>
<td>(V^+ - 1.5)</td>
<td></td>
<td>0</td>
<td>(V^+ - 1.5)</td>
<td>V</td>
</tr>
<tr>
<td>Supply Current</td>
<td>Over Full Temperature Range</td>
<td>1</td>
<td>2</td>
<td></td>
<td>1</td>
<td>2</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>(R_L = \infty) on All Op Amps</td>
<td>0.5</td>
<td>1.2</td>
<td></td>
<td>0.5</td>
<td>1.2</td>
<td>mA</td>
</tr>
</tbody>
</table>

### Electrical Characteristics

\(V^+ = +5.0\text{V},\) (Note 4), unless otherwise stated

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM158A</th>
<th>LM358A</th>
<th>LM158/LM258</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Large Signal Voltage Gain</td>
<td>(V^+ = 15\text{V}, T_A = 25^\circ\text{C}, R_L \geq 2 \text{k}\Omega, (\text{For } V_O = 1\text{V to } 11\text{V}))</td>
<td>50</td>
<td>100</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td>Common-Mode Rejection Ratio</td>
<td>(T_A = 25^\circ\text{C}, V_{\text{CM}} = 0\text{V to } V^+ - 1.5\text{V})</td>
<td>70</td>
<td>85</td>
<td>70</td>
<td>85</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>(V^+ = 5\text{V to } 30\text{V}) (LM2904, (V^+ = 5\text{V to } 26\text{V}, T_A = 25^\circ\text{C})</td>
<td>65</td>
<td>100</td>
<td>65</td>
<td>100</td>
</tr>
<tr>
<td>Amplifier-to-Amplifier Coupling</td>
<td>(f = 1 \text{kHz to } 20 \text{kHz}, T_A = 25^\circ\text{C}) (Input Referred), (Note 8)</td>
<td>-120</td>
<td>-120</td>
<td>-120</td>
<td>-120</td>
</tr>
<tr>
<td>Output Current Source</td>
<td>(V_{\text{IN}^{+}} = 1\text{V}, V_{\text{IN}^{-}} = 0\text{V}, V^+ = 15\text{V}, V_O = 2\text{V}, T_A = 25^\circ\text{C})</td>
<td>20</td>
<td>40</td>
<td>20</td>
<td>40</td>
</tr>
<tr>
<td></td>
<td>(V_{\text{IN}^{+}} = 1\text{V}, V_{\text{IN}^{-}} = 0\text{V}, V^+ = 15\text{V}, V_O = 2\text{V})</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td>(V_{\text{IN}^{+}} = 1\text{V}, V_{\text{IN}^{-}} = 0\text{V}, T_A = 25^\circ\text{C}, V_O = 200 \text{mV}, V^+ = 15\text{V})</td>
<td>12</td>
<td>50</td>
<td>12</td>
<td>50</td>
</tr>
<tr>
<td>Short Circuit to Ground</td>
<td>(T_A = 25^\circ\text{C}, (Note 2), V^+ = 15\text{V})</td>
<td>40</td>
<td>60</td>
<td>40</td>
<td>60</td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>(Note 5)</td>
<td>4</td>
<td>5</td>
<td>7</td>
<td>mV</td>
</tr>
<tr>
<td>Input Offset Voltage Drift</td>
<td>(R_S = 0\Omega)</td>
<td>7</td>
<td>15</td>
<td>7</td>
<td>20</td>
</tr>
<tr>
<td>Input Offset Current</td>
<td>(I_{\text{IN}(+) - I_{\text{IN}(-)}, V_{\text{CM}} = 0\text{V}, T_A = 25^\circ\text{C}})</td>
<td>30</td>
<td>75</td>
<td>100</td>
<td>nA</td>
</tr>
<tr>
<td>Input Offset Current Drift</td>
<td>(R_S = 0\Omega)</td>
<td>10</td>
<td>200</td>
<td>10</td>
<td>300</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>(I_{\text{IN}(+) or I_{\text{IN}(-)}}, V_{\text{CM}} = 0\text{V}, T_A = 25^\circ\text{C}})</td>
<td>40</td>
<td>100</td>
<td>40</td>
<td>200</td>
</tr>
<tr>
<td>Input Common-Mode Voltage Range</td>
<td>(V^+ = 30\text{V}, (Note 7)) (LM2904, (V^+ = 26\text{V}))</td>
<td>0</td>
<td>(V^+ - 2)</td>
<td>0</td>
<td>(V^+ - 2)</td>
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</table>
### Electrical Characteristics (Continued)

$V^+ = +5.0V$, (Note 4), unless otherwise stated

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<th>LM358A</th>
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<th>LM158/LM258</th>
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<th>Units</th>
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<tbody>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
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<td>Max</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
<td></td>
</tr>
<tr>
<td><strong>Large Signal Voltage</strong></td>
<td>$V^+ = +15V$, (Note 4), R_L ≥ 2 kΩ</td>
<td>25</td>
<td>15</td>
<td>25</td>
<td>25</td>
<td>15</td>
<td>25</td>
<td>25</td>
<td>15</td>
<td>25</td>
<td>V/mV</td>
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<tr>
<td><strong>Gain</strong></td>
<td>$V_{CH} = +30V$, (Note 4), R_L = 2 kΩ</td>
<td>26</td>
<td>26</td>
<td>26</td>
<td>27</td>
<td>28</td>
<td>27</td>
<td>27</td>
<td>27</td>
<td>27</td>
<td>V</td>
</tr>
<tr>
<td><strong>Output Voltage</strong></td>
<td>$V_{CL} = +5V$, R_L = 10 kΩ</td>
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<td>5</td>
<td>20</td>
<td>5</td>
<td>20</td>
<td>5</td>
<td>20</td>
<td>5</td>
<td>mV</td>
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<tr>
<td><strong>Output Current</strong></td>
<td>Source: $V_{IN}^+ = +1V$, $V_{IN}^- = 0V$, $V^+ = 15V$, $V_O = 2V$, $T_A = 25^\circ C$</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>Sink: $V_{IN}^- = +1V$, $V_{IN}^+ = 0V$, $V^+ = 15V$, $V_O = 2V$, $T_A = 25^\circ C$</td>
<td>10</td>
<td>15</td>
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<td>5</td>
<td>8</td>
<td>5</td>
<td>8</td>
<td>5</td>
<td>mA</td>
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### Electrical Characteristics

$V^+ = +5.0V$, (Note 4), unless otherwise stated

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<th>Conditions</th>
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<td>Typ</td>
<td>Max</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
<td></td>
</tr>
<tr>
<td><strong>Large Signal Voltage</strong></td>
<td>$V^+ = 15V$, $T_A = 25^\circ C$, R_L ≥ 2 kΩ, (For $V_O = 1V$ to 11V)</td>
<td>25</td>
<td>100</td>
<td>25</td>
<td>100</td>
<td>25</td>
<td>100</td>
<td>V/mV</td>
</tr>
<tr>
<td><strong>Common-Mode Rejection Ratio</strong></td>
<td>$T_A = 25^\circ C$, $V_{CM} = 0V$ to $-1.5V$</td>
<td>65</td>
<td>85</td>
<td>50</td>
<td>70</td>
<td>50</td>
<td>70</td>
<td>dB</td>
</tr>
<tr>
<td><strong>Power Supply Rejection Ratio</strong></td>
<td>$V^+ = 5V$ to $30V$, (Note 4), $V_{CM} = 0V$ to $26V$, $T_A = 25^\circ C$</td>
<td>65</td>
<td>100</td>
<td>50</td>
<td>100</td>
<td>50</td>
<td>100</td>
<td>dB</td>
</tr>
<tr>
<td><strong>Amplifier-to-Amplifier Coupling</strong></td>
<td>Coupling: $f = 1 kHz$ to $20 kHz$, $T_A = 25^\circ C$ (Input Referred), (Note 8)</td>
<td>$-120$</td>
<td>$-120$</td>
<td>$-120$</td>
<td>$-120$</td>
<td>$-120$</td>
<td>$-120$</td>
<td>dB</td>
</tr>
<tr>
<td><strong>Output Current</strong></td>
<td>Source: $V_{IN}^+ = 1V$, $V_{IN}^- = 0V$, $V^+ = 15V$, $V_O = 2V$, $T_A = 25^\circ C$</td>
<td>20</td>
<td>40</td>
<td>20</td>
<td>40</td>
<td>20</td>
<td>40</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>Sink: $V_{IN}^- = 1V$, $V_{IN}^+ = 0V$, $V^+ = 15V$, $V_O = 2V$, $T_A = 25^\circ C$</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
<td>10</td>
<td>20</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>$V_{IN}^- = 1V$, $V_{IN}^+ = 0V$, $T_A = 25^\circ C$, $V_O = 200 mV$, $V^+ = 15V$</td>
<td>12</td>
<td>50</td>
<td>12</td>
<td>50</td>
<td>12</td>
<td>50</td>
<td>µA</td>
</tr>
<tr>
<td><strong>Short Circuit to Ground</strong></td>
<td>$T_A = 25^\circ C$, (Note 2), $V^+ = 15V$</td>
<td>40</td>
<td>60</td>
<td>40</td>
<td>60</td>
<td>40</td>
<td>60</td>
<td>mA</td>
</tr>
<tr>
<td><strong>Input Offset Voltage</strong></td>
<td>(Note 5)</td>
<td>9</td>
<td>10</td>
<td>9</td>
<td>10</td>
<td>9</td>
<td>10</td>
<td>mV</td>
</tr>
<tr>
<td><strong>Input Offset Voltage Drift</strong></td>
<td>$R_S = 0\Omega$</td>
<td>7</td>
<td>7</td>
<td>7</td>
<td>7</td>
<td>7</td>
<td>7</td>
<td>µV/°C</td>
</tr>
<tr>
<td><strong>Input Offset Current</strong></td>
<td>$I_{IN(+)} - I_{IN(-)}$</td>
<td>150</td>
<td>45</td>
<td>200</td>
<td>45</td>
<td>200</td>
<td>45</td>
<td>nA</td>
</tr>
<tr>
<td><strong>Input Offset Current Drift</strong></td>
<td>$R_S = 0\Omega$</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>pA/°C</td>
</tr>
<tr>
<td><strong>Input Bias Current</strong></td>
<td>$I_{IN(+)}$ or $I_{IN(-)}$</td>
<td>40</td>
<td>500</td>
<td>40</td>
<td>500</td>
<td>40</td>
<td>500</td>
<td>nA</td>
</tr>
<tr>
<td><strong>Input Common-Mode Voltage Range</strong></td>
<td>$V^+ = 30V$, (Note 7), (LM2904, $V^+ = 26V$)</td>
<td>0</td>
<td>$V^+ - 2$</td>
<td>0</td>
<td>$V^+ - 2$</td>
<td>0</td>
<td>$V^+ - 2$</td>
<td>V</td>
</tr>
</tbody>
</table>
**Electrical Characteristics (Continued)**

V+ = +5.0V, (Note 4), unless otherwise stated

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM358</th>
<th>LM2904</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Large Signal Voltage</td>
<td>V+ = +15V</td>
<td></td>
<td>15</td>
<td></td>
</tr>
<tr>
<td>Gain (V+O = 1V to 11V)</td>
<td>R_L ≥ 2 kΩ</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Voltage Swing</td>
<td>V_RH = +30V</td>
<td>26</td>
<td>22</td>
<td></td>
</tr>
<tr>
<td></td>
<td>(LM2904, V+ = 26V)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>R_L = 2 kΩ</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>V_RL = 10 kΩ</td>
<td>27</td>
<td>28</td>
<td>23</td>
</tr>
<tr>
<td></td>
<td>R_L = 10 kΩ</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Current Source</td>
<td>V_IN+ = 0V, V_IN− = 0V,</td>
<td>10</td>
<td>20</td>
<td></td>
</tr>
<tr>
<td></td>
<td>V+ = 15V, V_O = 0V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>V+ = 5V, R_L = 10 kΩ</td>
<td>5</td>
<td>20</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Current Sink</td>
<td>V_IN− = 0V, V_IN+ = 0V,</td>
<td>5</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td></td>
<td>V+ = 15V, V_O = 2V</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Note 1:** For operating at high temperatures, the LM358/LM358A, LM2904 must be derated based on a +125˚C maximum junction temperature and a thermal resistance of 120˚C/W for MDIP, 182˚C/W for Metal Can, 189˚C/W for Small Outline package, and 230˚C/W for micro SMD, which applies for the device soldered in a printed circuit board, operating in a still air ambient. The LM258/LM258A and LM158/LM158A can be derated based on a +150˚C maximum junction temperature. The dissipation is the total of both amplifiers—use external resistors, where possible, to allow the amplifier to saturate or to reduce the power which is dissipated in the integrated circuit.

**Note 2:** Short circuits from the output to V+ can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of V+. At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

**Note 3:** This current input will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the op amps to go to the V+ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than −0.3V (at 25˚C).

**Note 4:** These specifications are limited to −55˚C ≤ T_A ≤ +125˚C for the LM158/LM158A. With the LM258/LM258A, all temperature specifications are limited to −25˚C ≤ T_A ≤ +85˚C, the LM358/LM358A temperature specifications are limited to 0˚C ≤ T_A ≤ +70˚C, and the LM2904 specifications are limited to −40˚C ≤ T_A ≤ +85˚C.

**Note 5:** The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.

**Note 7:** The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (at 25˚C). The upper end of the common-mode voltage range is V+ − 1.5V (at 25˚C), but either or both inputs can go to +32V without damage (+26V for LM2904), independent of the magnitude of V+.

**Note 8:** Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies.

**Note 9:** Refer to RETS158AX for LM158A military specifications and to RETS158X for LM158 military specifications.

**Note 10:** Human body model, 1.5 kΩ in series with 100 pF.

**Typical Performance Characteristics**

**Input Voltage Range**

**Input Current**

![Diagram](DS007787-34)

![Diagram](DS007787-35)
Typical Performance Characteristics (Continued)

Supply Current

Voltage Gain

Open Loop Frequency Response

Common-Mode Rejection Ratio

Voltage Follower Pulse Response

Voltage Follower Pulse Response (Small Signal)
**Typical Performance Characteristics (Continued)**

**Application Hints**

The LM158 series are op amps which operate with only a single power supply voltage, have true-differential inputs, and remain in the linear mode with an input common-mode voltage of 0 VDC. These amplifiers operate over a wide range of power supply voltage with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 2.3 VDC.

Precautions should be taken to ensure that the power supply for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a test socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.

Large differential input voltages can be easily accommodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger...
Application Hints (Continued)

than \( V^+ \) without damaging the device. Protection should be provided to prevent the input voltages from going negative more than \(-0.3\) VDC (at 25˚C). An input clamp diode with a resistor to the IC input terminal can be used.

To reduce the power supply current drain, the amplifiers have a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents. Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

For ac applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and prevent crossover distortion. Where the load is directly coupled, as in dc applications, there is no crossover distortion.

Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50 pF can be accommodated using the worst-case non-inverting unity gain connection. Large closed loop gains or resistive isolation should be used if larger load capacitance must be driven by the amplifier.

The bias network of the LM158 establishes a drain current which is independent of the magnitude of the power supply voltage over the range of 3 VDC to 30 VDC.

Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive function temperatures. Putting direct short-circuits on more than one amplifier at a time will increase the total IC power dissipation to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers. The larger value of output source current which is available at 25˚C provides a larger output current capability at elevated temperatures (see typical performance characteristics) than a standard IC op amp.

The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used. In general, introducing a pseudo-ground (a bias voltage reference of \( V^+/2 \)) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.
### Ordering Information

<table>
<thead>
<tr>
<th>Package</th>
<th>Temperature Range</th>
<th>NSC Drawing</th>
</tr>
</thead>
<tbody>
<tr>
<td>SO-8</td>
<td>−55˚C to 125˚C</td>
<td>LM2904M</td>
</tr>
<tr>
<td></td>
<td>−25˚C to 85˚C</td>
<td>LM358AM</td>
</tr>
<tr>
<td></td>
<td>0˚C to 70˚C</td>
<td>LM358AMX</td>
</tr>
<tr>
<td></td>
<td>−40˚C to 85˚C</td>
<td>LM358M</td>
</tr>
<tr>
<td></td>
<td></td>
<td>LM358MX</td>
</tr>
<tr>
<td>8-Pin Molded DIP</td>
<td></td>
<td>M08A</td>
</tr>
<tr>
<td>8-Pin Ceramic DIP</td>
<td>LM358AN</td>
<td>N08E</td>
</tr>
<tr>
<td>TO-5, 8-Pin Metal Can</td>
<td>LM358BP</td>
<td>H08C</td>
</tr>
</tbody>
</table>

**Note 11:** LM158 is available per SMD #5962-8771001
LM158A is available per SMD #5962-8771002

**Note 12:** See STD Mil DWG 5962L87710 for Radiation Tolerant Devices
**Typical Single-Supply Applications \((V^+ = 5.0 \, V_{DC})\)**

**Non-Inverting DC Gain (0V Output)**

\[
\text{Gain} = 1 + \frac{R_2}{R_1} = 101 \text{ (as shown)}
\]

*\(R\) not needed due to temperature independent \(I_{IN}\)

**DC Summing Amplifier\((V_{IN'S} \geq 0 \, V_{DC} \text{ and } V_O \geq 0 \, V_{DC})\)**

Where: \(V_O = V_1 + V_2 + V_3 + V_4\)

\((V_1 + V_2) \geq (V_3 + V_4)\) to keep \(V_O > 0 \, V_{DC}\)

**Power Amplifier**

\(V_O = 0 \, V_{DC} \text{ for } V_{IN} = 0 \, V_{DC}\)

\(A_V = 10\)
Typical Single-Supply Applications \((V^+ = 5.0 \ V_{DC})\) (Continued)

"BI-QUAD" RC Active Bandpass Filter

\[ f_0 = 1 \ \text{kHz} \]
\[ Q = 50 \]
\[ A_v = 100 \ (40 \ \text{dB}) \]

Fixed Current Sources

\[ I_2 = \frac{R_1}{R_2} \ I_1 \]

Lamp Driver

\[ V^+ \rightarrow 30 \ \text{mA} \rightarrow 600 \ \text{mA} \]

www.national.com
Typical Single-Supply Applications \( (V^+ = 5.0 \, V_{DC}) \) (Continued)

**LED Driver**

\[
\text{DS007787-13}
\]

\[
\text{1/2 LM358} \quad 20 \, \text{mA}
\]

\[
\begin{align*}
R_2 & \quad \text{IL small} \\
V_L & \leq V^+ - 2V
\end{align*}
\]

**Current Monitor**

\[
\text{DS007787-14}
\]

\[
\begin{align*}
V_O &= \frac{1V(\text{IL})}{\text{IL}} \\
R_1^* &= 0.1 \Omega \\
R_2 &= 10\,\Omega \\
R_3 &= 1k
\end{align*}
\]

* (Increase R1 for I_L small)

\[
V_L \leq V^+ - 2V
\]

**Driving TTL**

\[
\text{DS007787-15}
\]

\[
\begin{align*}
1/2 \, \text{LM358} \\
R_L &= 240 \, \Omega \\
1/4 \, \text{DM54XX}
\end{align*}
\]

**Voltage Follower**

\[
\text{DS007787-17}
\]

\[
V_O = V_{IN}
\]

**Pulse Generator**

\[
\text{DS007787-16}
\]

\[
\begin{align*}
R_1 &= 1M \\
R_2 &= 100k \\
R_3 &= 100k \\
R_4 &= 100k \\
R_5 &= 100k \\
V^+ &\quad \text{IN914}
\end{align*}
\]

\[
V_O
\]

\[
0
\]
Typical Single-Supply Applications \( (V^+ = 5.0 \, V_{DC}) \) (Continued)

Squarewave Oscillator

Pulse Generator

Low Drift Peak Detector

HIGH \( Z_{IN} \)
LOW \( Z_{OUT} \)
Typical Single-Supply Applications \( (V^+ = 5.0 \, V_{DC}) \) (Continued)

High Compliance Current Sink

![High Compliance Current Sink Diagram]

\( I_O = 1 \text{ amp/volt } V_{IN} \)
(Increase \( R_E \) for \( I_O \) small)

Comparator with Hysteresis

![Comparator with Hysteresis Diagram]

Voltage Controlled Oscillator (VCO)

![Voltage Controlled Oscillator Diagram]

*WIDE CONTROL VOLTAGE RANGE: \( 0 \, V_{DC} \leq V_C \leq 2 \, (V^+ - 1.5 \, V_{DC}) \)
Typical Single-Supply Applications \( (V^+ = 5.0 \, V_{DC}) \) (Continued)

**AC Coupled Inverting Amplifier**

\[ A_V = \frac{R_1}{R_2} \quad \text{(As shown, } A_V = 10) \]

**Ground Referencing a Differential Input Signal**
Typical Single-Supply Applications \((V^+ = 5.0 \text{ V}_{\text{DC}}\) (Continued)

**AC Coupled Non-Inverting Amplifier**

\[
\begin{align*}
A_V &= 1 + \frac{R_2}{R_1} \\
A_v &= 11 \text{ (As Shown)}
\end{align*}
\]

**DC Coupled Low-Pass RC Active Filter**

\[
\begin{align*}
f_0 &= 1 \text{ kHz} \\
Q &= 1 \\
A_V &= 2
\end{align*}
\]
Typical Single-Supply Applications \((V^+ = 5.0 \, V_{DC})\) (Continued)

Bandpass Active Filter

![Bandpass Active Filter Diagram](DS00787-28)

- \(f_0 = 1 \, \text{kHz}\)
- \(Q = 25\)

High Input Z, DC Differential Amplifier

![High Input Z, DC Differential Amplifier Diagram](DS00787-29)

For \(\frac{R_1}{R_2} = \frac{R_4}{R_3}\) (CMRR depends on this resistor ratio match)

\[V_O = 1 + \frac{R_4}{R_3} (V_2 - V_1)\]

As Shown: \(V_O = 2 (V_2 - V_1)\)
Typical Single-Supply Applications \((V^+ = 5.0 \, V_{DC})\) (Continued)

**Photo Voltaic-Cell Amplifier**

\[ I_{CELL} \]

\[ \left( \text{CELL HAS 0V ACROSS IT} \right) \]

\[ \frac{1}{2} \text{LM358} \]

\[ V_O \]

**Bridge Current Amplifier**

\[ +V_{REF} \]

\[ \frac{R_T}{1 + \delta} \]

\[ \frac{1}{2} \text{LM358} \]

\[ V_O \]

For \( \delta < 1 \) and \( R_T >> R \)

\[ V_O = V_{REF} \left( \frac{\delta}{2} \right) \frac{R_T}{R} \]

**High Input Z Adjustable-Gain DC Instrumentation Amplifier**

\[ +V_1 \]

\[ R_1 \]

\[ 100k \]

\[ + \]

\[ - \]

\[ \text{GAIN ADJUST} \]

\[ R_2 \]

\[ 2k \]

\[ \frac{1}{2} \text{LM358} \]

\[ R_3 \]

\[ 100k \]

\[ V_O \]

\[ + \]

\[ R_4 \]

\[ 100k \]

\[ + \]

\[ - \]

\[ \frac{1}{2} \text{LM358} \]

\[ V_O \]

\[ + \]

\[ R_5 \]

\[ 100k \]

\[ + \]

\[ - \]

\[ R_6 \]

\[ 100k \]

\[ - \]

\[ R_7 \]

\[ 100k \]

\[ + \]

\[ +V_2 \]

\[ \text{If } R_1 = R_5 \, \text{and} \, R_3 = R_4 = R_6 = R_7 \, (\text{CMRR depends on match}) \]

\[ V_O = \frac{1 + \frac{2R_1}{R_2}}{\delta} (V_2 - V_1) \]

As shown \( V_O = 101 (V_2 - V_1) \)
Using Symmetrical Amplifiers to Reduce Input Current (General Concept)

Schematic Diagram (Each Amplifier)
Physical Dimensions  inches (millimeters) unless otherwise noted

Metal Can Package (H)
NS Package Number H08C

Cerdip Package (J)
NS Package Number J08A
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

Molded Dip Package (N)
Order Number LM358AN, LM358N or LM2904N
NS Package Number N08E

Order Number LM158AWG/883
NS Package Number W14B
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

NOTES: UNLESS OTHERWISE SPECIFIED
1. EPOXY COATING
2. 63Sn/37Pb EUTECTIC BUMP
3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.
4. PIN 1 IS ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION REMAINING PINS ARE NUMBERED COUNTERCLOCKWISE.
5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE X₁ IS PACKAGE WIDTH, X₂ IS PACKAGE LENGTH AND X₃ IS PACKAGE HEIGHT.
6. REFERENCE JEDEC REGISTRATION MO-211, VARIATION BC.

8-Bump micro SMD
NS Package Number BPA08AAB
X₁ = 1.285  X₂ = 1.285  X₃ = 0.850
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