Enclosed is a rather long bibliography of Multics documentation as of December 16, 1968. Since the volume of material precludes casual reading, the following short ordered list of especially relevant documents may be useful. (See the full bibliography for notes and comments on each.)

1. 1965 FJCC papers.
2. Definition of Initial Multics (C. Clingen).
3. Comparison of the Multics System and CTSS (Corbató).
7. Reprints of recent talks and papers.
External documents:

1. **Multics System Programmers' Manual (MSPM).** The ultimate reference for details of the design of Multics. Although it is continually being updated, many sections are not current. As a general rule, the sections labeled "overview" are accurate in at least general philosophy and overall strategy, although they may be behind the current design in detail.

2. **EPL User's Reference Manual.** Describes how to program in the compiler language which is used for most of the Multics System.

3. **Multics Operating System.** An early attempt to provide an overview of Multics, culled from now-obsolete MSPM overviews.

4. **645 System Manual.** A slick GE publication describing the objectives of the overall system. (Out of print.)

5. **EPLBSA User's Reference Handbook.** Description of the assembly language used for "hand-coded" Multics subroutines, and used as an intermediate step by the compiler for EPL.

6. **Handbook of Operating Information.** A collection of hardware word formats and status bit interpretation, primarily for the I/O and debugging specialist.


8. **Guide for Subsystem Writers, by E.I. Organick.** (In preparation) Chapters 1-3 have been widely distributed, and chapters 4-6 are in draft form. This guide provides a tutorial description of the aspects of Multics which are of most interest to a system programmer whose job it is to embed a subsystem within Multics. Put another way, this is a user's manual for sophisticated users.

9. **FL/I Manual.** Description of a macro-assembler planned for the 645.

10. **Fortran IV Manual.** The usual.

11. **Multics Condensed Guide.** A pocket-sized handbook to be carried to the console, containing a list of frequently used commands and how to use them.

12. **Multics Operator's Guide.** (In draft.) A guide for the machine room operator, telling him what buttons to push to get Multics started, etc.

13. **Project MAC Progress Report, II, III, and IV.** This yearly report of Project MAC contains a section describing in overview the significant events of the Multics project during the year. The Multics section of Progress Report V is available, although the full report has not yet been published.

14. **Multics User's Manual.** (In very early draft--barely begun, but the table of contents indicates well its intended scope.) The ordinary
Multics user will find this Manual his primary source of both overview information and reference information as he uses Multics as a programming tool.


Published papers:

1. Introduction and Overview of the Multics System, F.J. Corbató, V. Vyssotsky, 1965 FJCC.


The above six papers lay out the original objectives for Multics and the original plans for achieving those objectives. Although many of the proposed implementation details described here have been replaced by improved strategies, the overviews and descriptions of objectives are still basically current.


A description of the Multics protection ring scheme. This paper describes an advanced hardware design, not actually implemented on the GE 645 computer.


Moderately detailed description of how segmentation is implemented and used in Multics.


Overview paper giving some broad insights which have been acquired while designing Multics.
10. A Paging Experiment with the Multics System, to be published in a festschrift for P.M. Morse.

A description of the current page removal selection algorithm, and some experiments in generalizing it.


Edited transcription of a talk given describing the experience of having used PL/I in the implementation of Multics.


Edited transcription of a talk on the subject of managing the development of Multics-sized systems.

MIT Theses Related to Multics

Completed:


Design of the processor multiplexing section of Multics. Includes an overview of the entire system.


Implementation experiences in the processor multiplexing section of Multics, including insights into the topics of process creation and initialization of the address space.


Design of the planned (but not-yet installed) absentee user facility of Multics.


A PDP-8 program which sends commands down several telephone lines, with "think time" pauses between, thereby providing repeatable simulations of a user load on the time-sharing system.


A PDP-8 program which spies on Multics via a special interface, and displays (after interpreting) information it has found.


These two are self-explanatory.

Theses in progress:


   Goes with the Clark thesis, above, to provide a complete, modern compiler for the PL/I language (in principle).


   A model on paper, suitable for presentation to a class, of a system like Multics, only simpler. Many unnecessary complications of Multics (e.g., ability to run GECOS programs) are not present in this paper system, and the hardware presumed for it has been modified where necessary to make the operating program simpler.

Internal Documents; Technical:

1. **Multics Checkout Bulletins**. A series of bulletins distributed to all programmers telling them of current status of systems, lists of segments on current libraries, changes to the standard system, etc. A majority of these have a useful lifetime of about 1 week.

2. **Multics Operating Staff Notes**. A series of bulletins directed to the operators who man the 645 machine room. These contain timely information, usually installation-dependent, which does not appropriately fit in the operators guide to Multics.

3. **The Repository**. Three series of documents, mostly technical, issued by the three organizations, MIT, BTL, and GE. The Repository has been a home for design proposals, bright ideas, and things which it seemed should be recorded somewhere but did not have any other logical home. As the design has become complete, more and more documents have fallen into the specific documentation areas (e.g., Multics System Programmer's Manual) and the repository has been less and less used. Included in the GE series are the hardware specifications for the 645 processor, general I/O controller, system controller, and clock.

4. **Design Notebook**. A set of about 20 documents, since placed in the repository, which were the earliest design proposals for Multics. By now they are only of historical interest.

5. **Definition of Initial Multics**, by C.T. Clingen. A list of functional capabilities, and performance and reliability targets for the initial version of Multics.

6. **Comparison of the Multics System and CTSS**, by F.J. Corbató. A brief list of the important features of Multics in a side-by-side comparison with their counterparts (if any) in CTSS.