

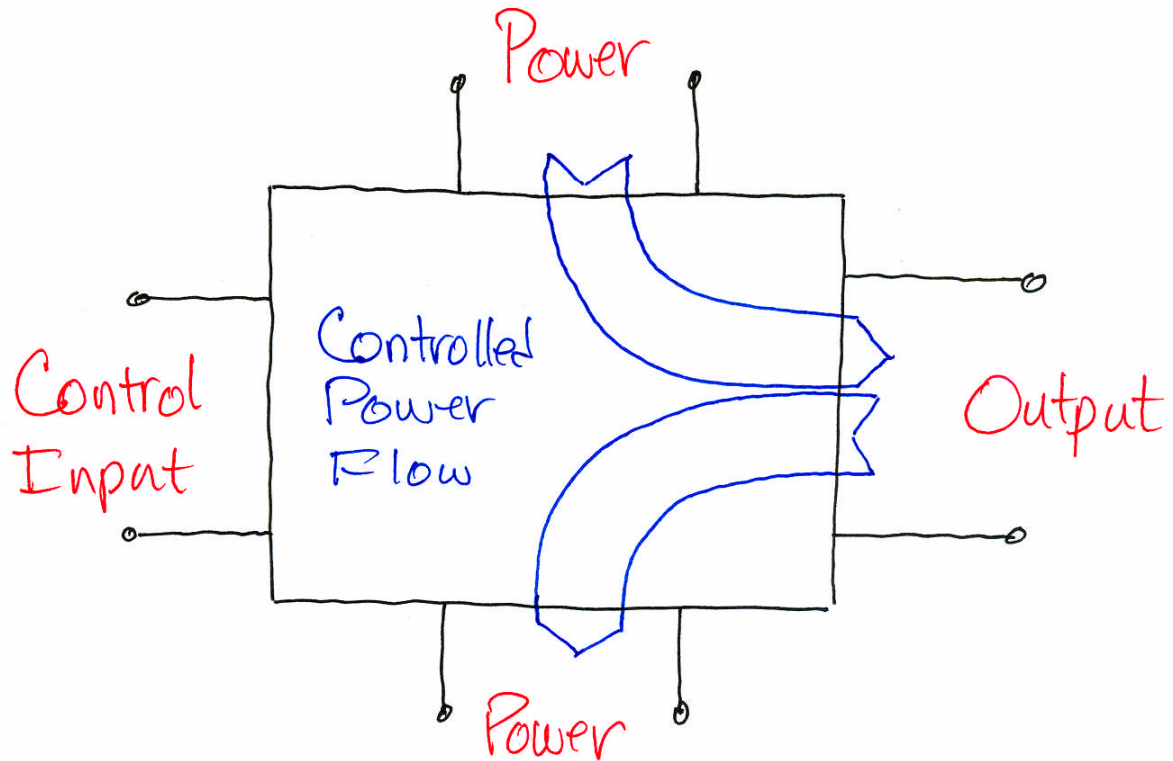
6.002 - Lecture 19

Operational Amplifiers



- Operational Amplifiers (Op Amps)
- Ideal Op Amp
- Non-idealities
- Op-Amp-Based Amplifiers

Amplifiers



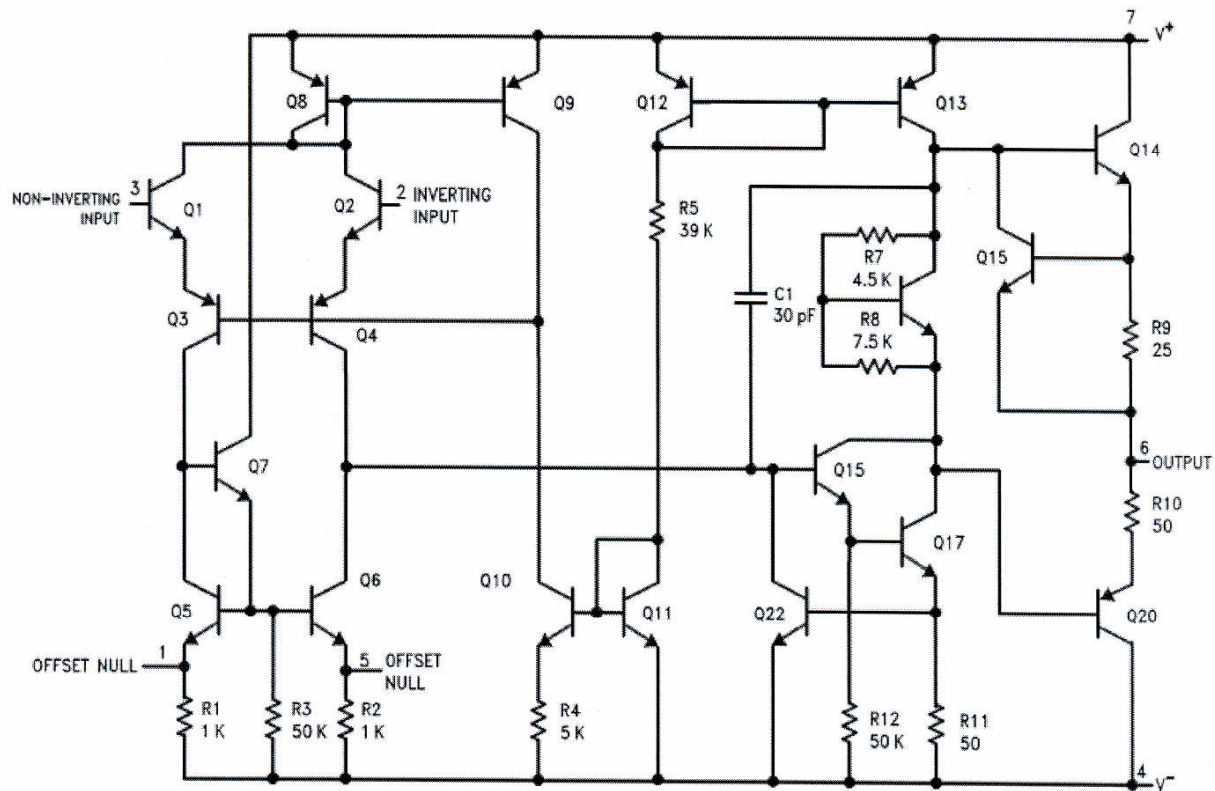
- Amplifier is a three- or four-port device.
- Provides voltage, current and/or power gain.
- Fundamental building block in analog signal processing systems such as: audio and video, communications, instrumentation and control, and so on.

Amplifier Wish List

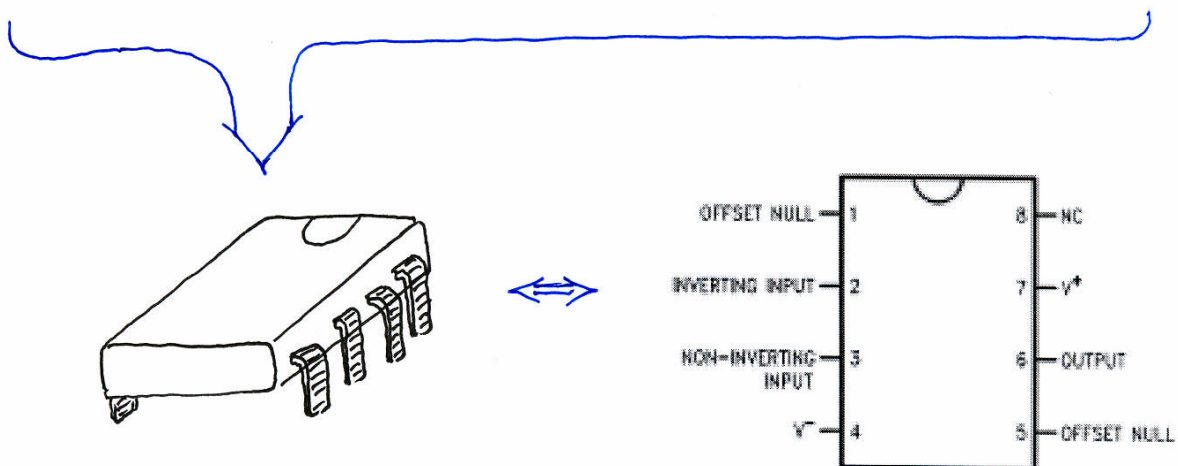
- High voltage, current and/or power gain
- Linear operation
- Wide bandwidth $\leftrightarrow$ fast operation
- Low noise
- High power handling capability
- Low loss
- Wide temperature operating range
- Differential input
- Single small package

Op amps provide many of these features.

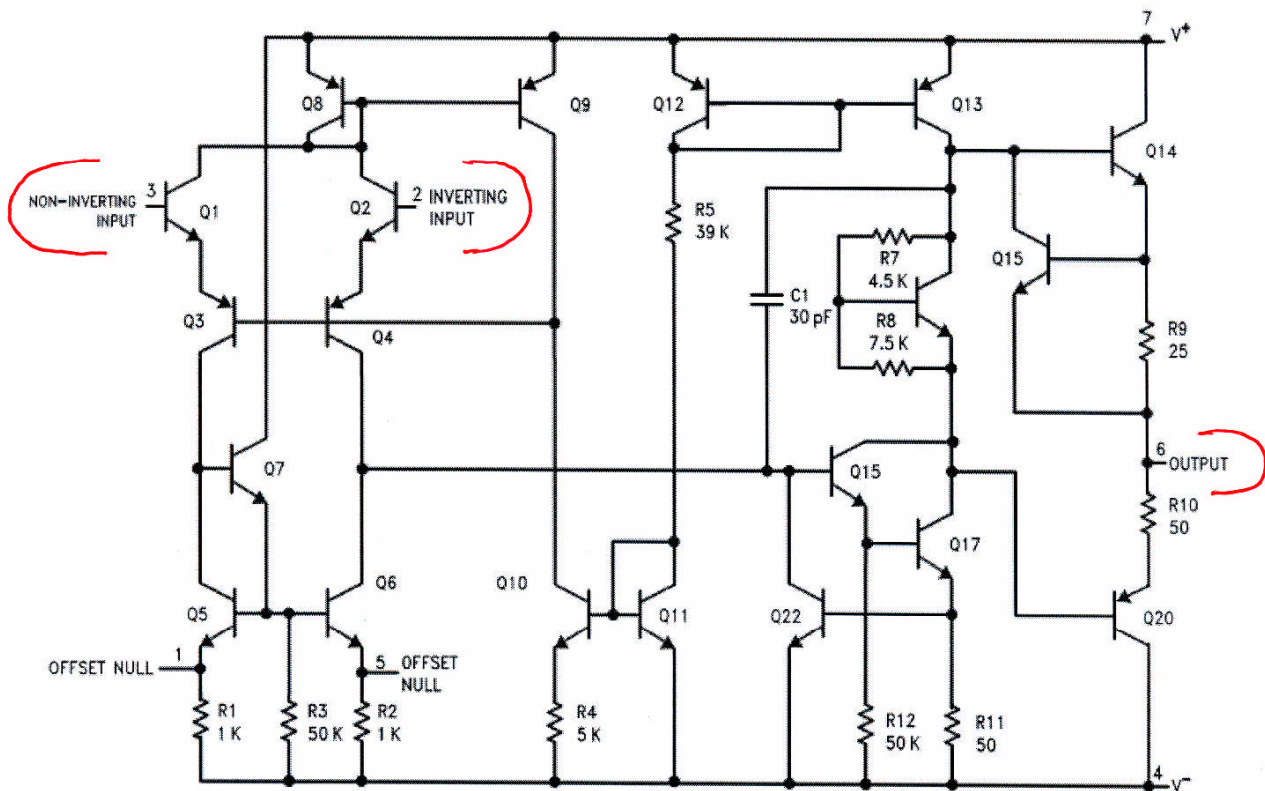
Operational Amplifier



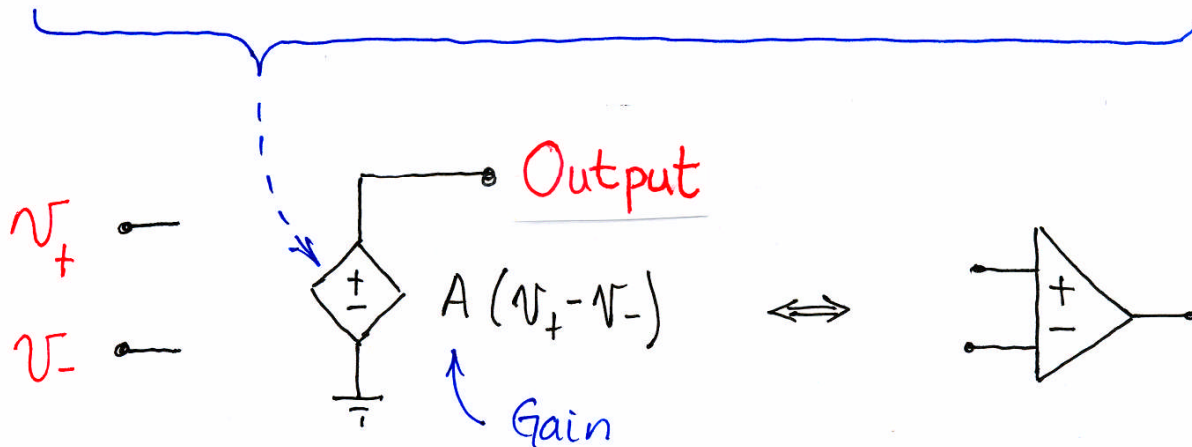
National Semiconductor LM741 Operational Amplifier



Simple Op - Amp Model

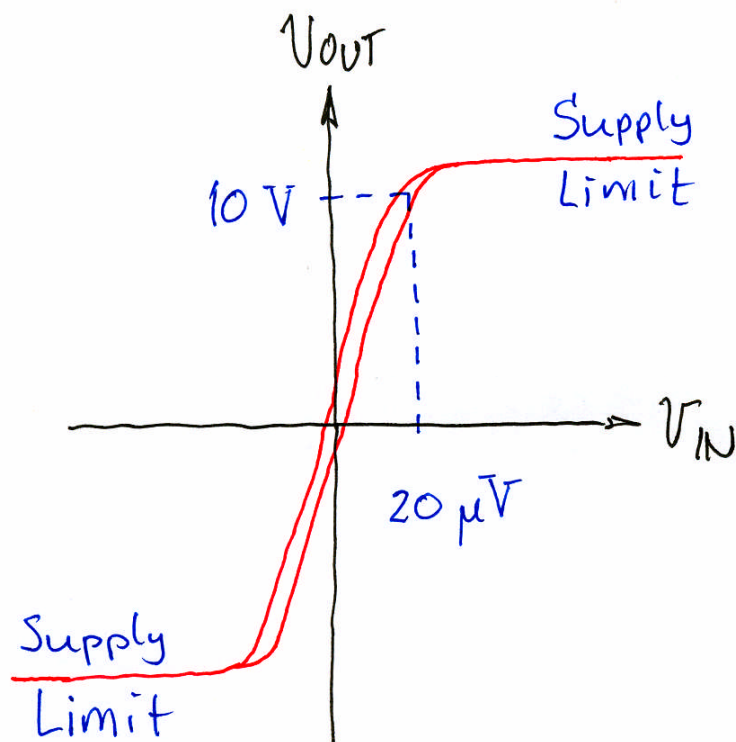
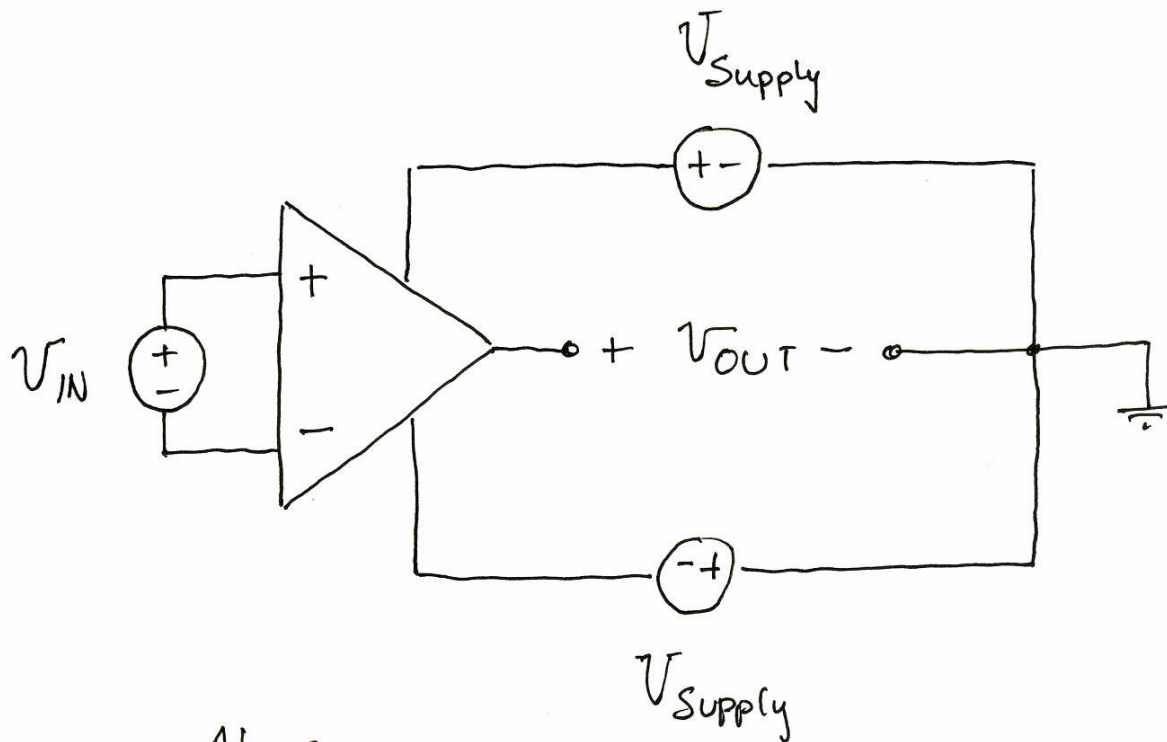


National Semiconductor LM741 Operational Amplifier



Power supply terminals not shown

Op - Amp Behavior



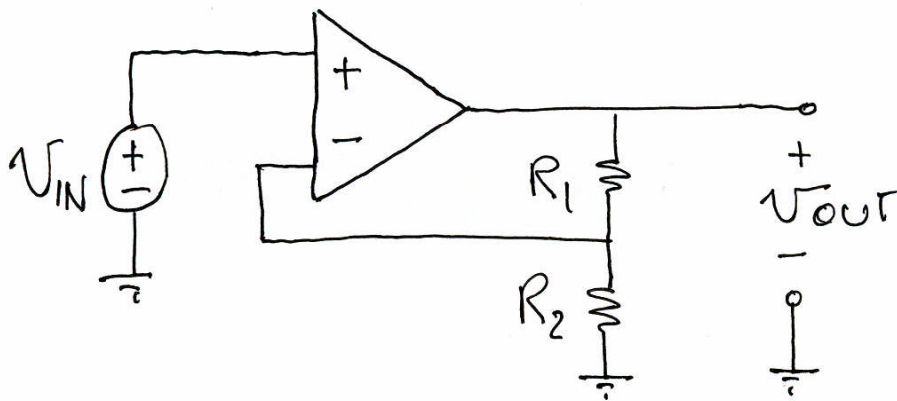
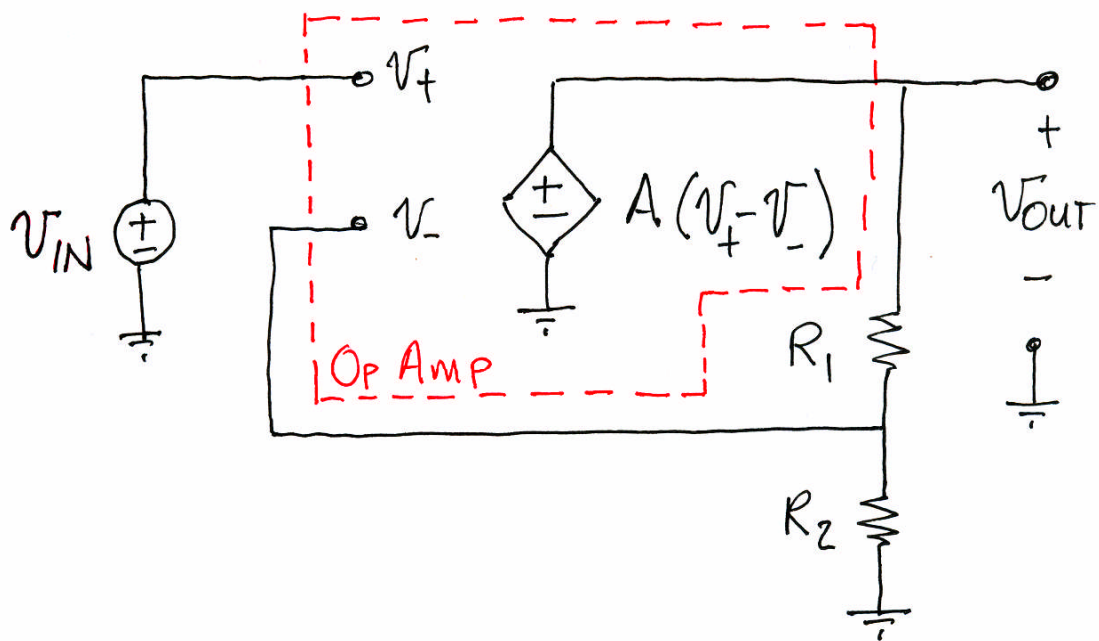
Voltage Gain $\approx 5 \cdot 10^5$

Non - Linear

Temperature Dependent

A Practical Amplifier

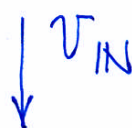
Use negative feedback to trade gain for more ideal performance.



Power supply connections not shown.

Analysis I

Node Method: $\frac{v_-}{R_2} + \frac{v_- - A(v_+ - v_-)}{R_1} = 0$



$$v_- = \frac{A R_2}{R_1 + (A+1) R_2} v_{IN}$$

$v_{out} = A(v_+ - v_-)$



$$= \frac{A \cdot G}{A + G} v_{IN} \quad G \equiv \frac{R_1 + R_2}{R_2}$$

If $A \gg G$ then $v_{out} = G v_{IN}$

↑
Dependent only
on resistors.

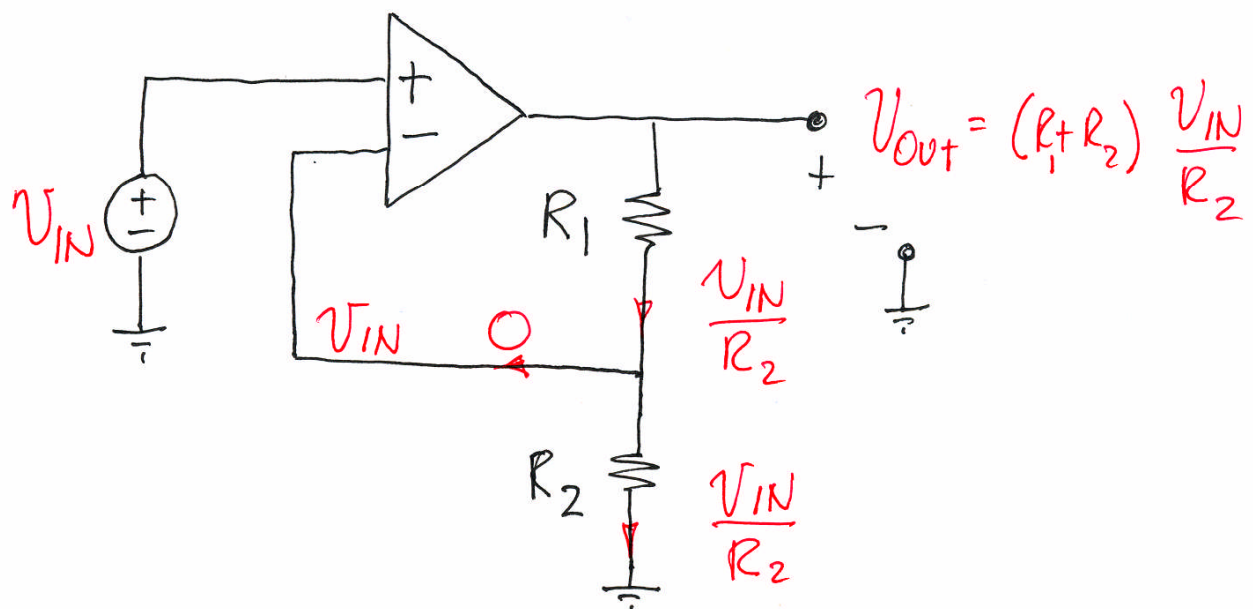
Analysis II

Assume $A \gg G$

$$V_+ - V_- = \frac{V_{out}}{A} \approx \frac{G V_{IN}}{A}$$

$$V_+ - V_- \ll V_{out}, V_{IN}$$

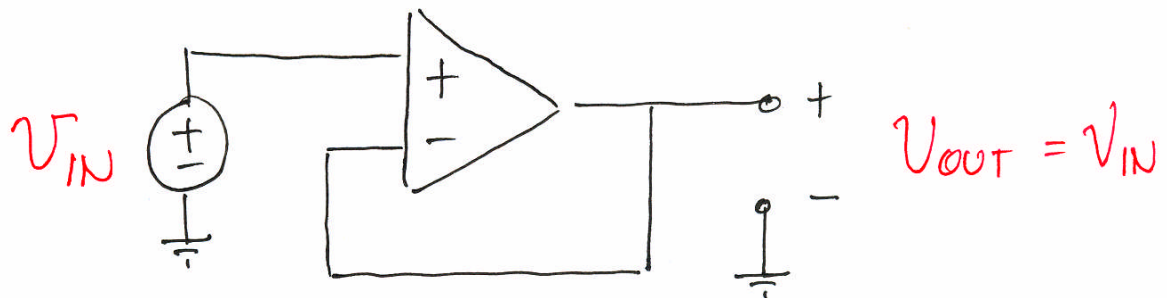
$$V_+ \approx V_-$$



Buffer

$R_1 \rightarrow 0$ (Short)

$R_2 \rightarrow \infty$ (Open)

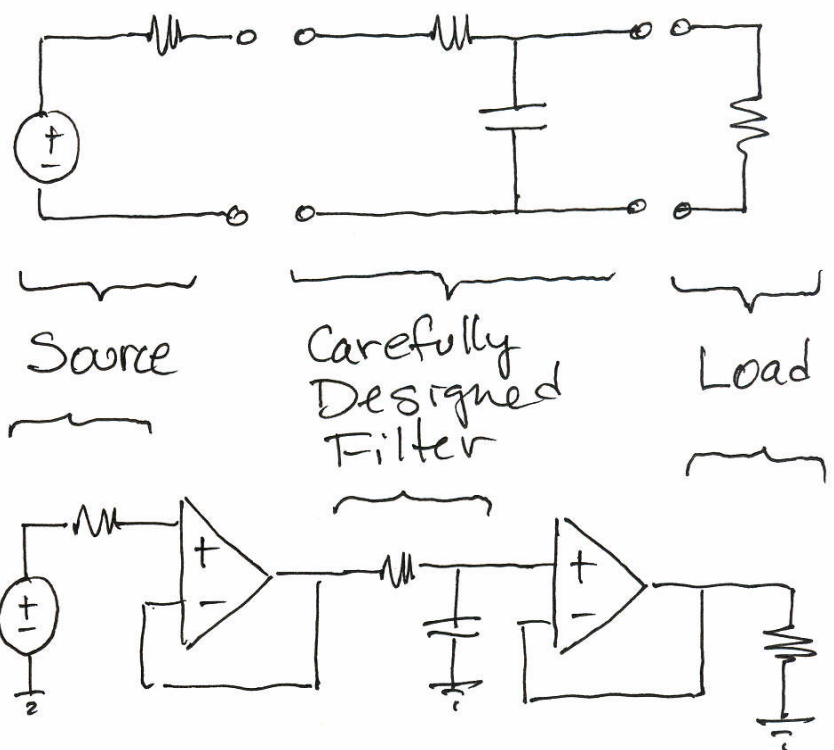
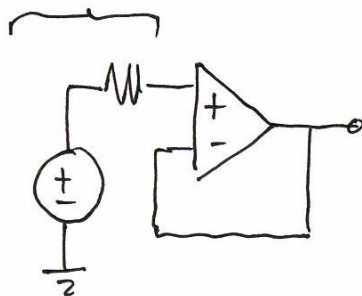


Voltage Gain = 1

Current Gain = ∞

Power Gain = ∞

Sensor



Non-idealities

- Finite, non-linear and non-differential gain.
- Finite bandwidth
- Temperature dependence
- Non-zero output resistance
- Finite input resistance
- Input offsets and noise

