6.002 At A Glance, Fall 2001

	Monday	Tuesday	Wednesday	Thursday	Friday
Sept	3 **Labor day**	4 **Registration Day**	5 Recitation: Variables: Voltage & Current; Elements: Resistors & Sources	6 Lecture 1: Course overview; Maxwell's Equations: KCL & KCL; Analysis **HW1 out**	7 Recitation: KVL & KCL, Series & Parallel, Resistive Dividers
Sept	10	11 Lecture 2: Potentials; Nodal Analysis	12 Recitation: Nodal Analysis Examples	13 Lecture 3: Linearity & Superposition; Thevenin & Norton **HW2 out**	14 Recitation: Thevenin & Norton Examples
Sept	17 **Student Holiday**	18 Lecture 4: Dependent Sources; Ports & Amplifiers	19 Recitation: Analysis with Dependent Sources	20 Lecture 5: Transistors & Models; Amplifiers & Switches **HW3 out**	21 Recitation: MOS transistor characteristics & models
Sept	24	25 Lecture 6: MOS Amplifiers & Biasing	26 Recitation: Practice with Load Lines	27 Lecture 7: Small-signal analysis **HW4 out**	28 Recitation: Small-signal analysis example; End of Q1 material
Oct **Lab 1**	1	2 Lecture 8: Digital Abstraction & Logic	3 Recitation: Digital Logic Examples	4 Lecture 9: MOS Switches & Digital Logic; Static Power **HW5 out**	5 Recitation: MOS Switch examples; **Add date**

Oct	8 **Columbus Day**	9 **Columbus Day**	10 **QUIZ 1**	11 Lecture 10: State & Memory; RC Networks & Step Response **HW6 out**	12 Recitation: RL Networks & Step Response
Oct	15	16 Lecture 11: Transient Response Relations & Impulses	17 Recitation: Digital Circuit Delay	18 Lecture 12: Digital Memory **HW7 out**	19 Recitation: Digital Memory
Oct **Lab 2**	22	23 Lecture 13: LC Networks & Responses	24 Recitation: RLC Networks & Responses	25 Lecture 14: Impedance & Admittance; e^{st} **HW8 out**	26 Recitation: Impedance & Admittance examples
Oct/Nov	29	30 Lecture 15: SSS & Frequency response; Poles & Zeros	31 Recitation: SSS & Frequency response examples	1 Lecture 16: Filters, RLC Network SSS & Q **HW9 out**	2 Recitation: Resonance; time-frequency response; End of Q2 material
Nov **Lab 3**	5	6 Lecture 17: Opamps & Feedback Amplifiers	7 Recitation: Opamp Inverting Amplifier	8 Lecture 18: Add, Subtract, Integrate, Differentiate **HW10 out**	9 Recitation: Opamp circuits & D-to-A
Nov	12 **Veteran's Day**	13 Lecture 19: Resonant Impedance Match—Synthesis	14 **QUIZ 2**	15 Lecture 20: Differential Amplifiers – How an Opamp works **HW11 out**	16 Recitation: Opamp filters

Nov	19	20 Lecture 21: Positive Feedback & Oscillation; Schmitt Trigger	21 Recitation: Positive Feedback & Oscillation **Drop date**	22 **Thanksgiving Holiday**	23 **Thanksgiving Holiday**
Nov **Lab 4**	26	27 Lecture 22: Energy & Power in Digital Logic	28 Recitation: CMOS examples	29 Lecture 23: Nonlinear: Diode switch or Attenuator **HW12 out**	30 Recitation: Assumed states; Diode wave shaper
Dec **Lab 4**	3	4 Lecture 24: Special topic: Clocks	5 Recitation: Impulse-driven oscillators and nonlinear oscillators	6 Lecture 25: Special topic: Dynamical Chaos	7 Recitation: Review
Dec	10	11 Lecture 26: Limitations of Lumped Parameter Abstractions	12 Recitation: Last Class: Review of course	13	14
	FINAL				