

DEPARTMENT OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE	
MASSACHUSETTS INSTITUTE OF TECHNOLOGY	
CAMBRIDGE, MASSACHUSETTS 02139	
6.101 Course Outline & Reading Assignments	Electronic Circuit Analysis and Design, 3rd Edition Donald A. Neaman
Content	
1. Components	
<input type="checkbox"/> resistors and capacitors standard values	pp 1331 to 1333,
<input type="checkbox"/> component symbols	handout
<input type="checkbox"/> frequency response, bode plots, basics review:	sections 7.1 to 7.2.4
2. Diodes	
<input type="checkbox"/> diodes, diode equation	1.2.4 to 1.3
<input type="checkbox"/> graphical/load line analysis	1.3.1
<input type="checkbox"/> diode models	1.3.2 to 1.3.4
<input type="checkbox"/> ideal	
<input type="checkbox"/> piecewise linear	
<input type="checkbox"/> AC	1.4 all
<input type="checkbox"/> other diode types	1.5.1 to 1.5.4,
<input type="checkbox"/> zener diodes	1.5.5
<input type="checkbox"/> diode applications	2.1 to 2.4.1, 2.5 to 2.6,
<input type="checkbox"/> peak sampler, power rectifier, clamps, regulator	handouts
3. Bipolar Transistors	
<input type="checkbox"/> definitions	p 292 to 5.1.4
<input type="checkbox"/> v-i characteristics, breakdown	5.1.5 to 5.1.6
<input type="checkbox"/> common-emitter large signal model, graphical analysis	5.2 to 5.2.3, 5.3.3
<input type="checkbox"/> common-collector	6.6 to 6.6.3
<input type="checkbox"/> common-emitter	6.4 to 6.4.4
<input type="checkbox"/> applications: current source, dc power supply regulator	
4. Bipolar Transistors	
<input type="checkbox"/> transistor biasing	5.4 to 5.4.3
<input type="checkbox"/> hybrid-pi equivalent circuit	6.2.2 to 6.2.4
<input type="checkbox"/> high-frequency hybrid-pi	7.4 to 7.4.5
<input type="checkbox"/> h-parameters	6.2.5
<input type="checkbox"/> common-emitter amplifier	6.3 to 6.4.4
<input type="checkbox"/> AC load line	6.5
<input type="checkbox"/> common-collector (emitter-follower) amplifier	6.6 to 6.6.3
5. Junction Field-Effect Transistors	
<input type="checkbox"/> operation	3.6 to 3.6.1
<input type="checkbox"/> background & v-i characteristics: JFET	3.6.2
<input type="checkbox"/> FET switch, chopper, MUX	
<input type="checkbox"/> low frequency incremental model	4.9 to 4.9.2
<input type="checkbox"/> biasing	3.6.3
<input type="checkbox"/> JFET current source	10.2.4
MOSFET	
<input type="checkbox"/> background & v-i characteristics	4.1 to 4.2
<input type="checkbox"/> Common Source Amplifier	4.3

□ Common Drain (Source Follower)	4.4
□ Common Gate & Summary	4.5 to 4.6
□ Amplifiers	4.7 to 4.11
6. Two-Transistor Amplifiers	
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□ current mirror	10.1 to 10.1.1
□ complementary emitter-follower [Class B, AB]	8.5 to 8.5.2
□ amplifier classes	8.3 to 8.3.4
□ power amplifiers	8.1 to 8.2.1, 8.2.4
7. Operational Amplifiers	
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□ basic linear op-amp circuits	
□ inverting, non-inverting, addition, subtraction	9.2 to 9.2.2; 9.3; 9.4 to 9.4.2; 9.7.1
□ ac amplifiers, inverting & non-inverting	
□ cascading; ideal impedances	
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□ integrator, differentiator	9.5.5
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8. Operational Amplifiers	
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□ input bias current, input offset current	14.5 to 14.5.2
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□ output voltage swing, saturation	p 1007
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11. Practical Matters	
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